Photonic Reservoir Computing: A Brain-Inspired Paradigm for Information Processing

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Abstract: In this talk, we will discuss how silicon chips can be used as hardware platform to implement neural-network-like structures, that can be used for applications such as Boolean operations with memory, header recognition and speech recognition.

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Abstract

In this talk, we will discuss how silicon chips can be used as hardware platform to implement neural-network-like structures, that can be used for applications such as Boolean operations with memory, header recognition and speech recognition. Article not available.

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