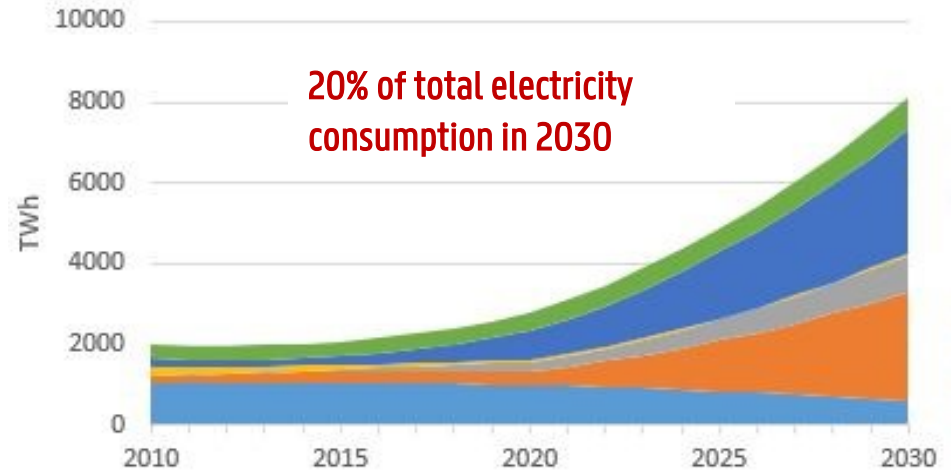


III-V-ON-SI TRANSCEIVERS BASED ON MICRO-TRANSFER-PRINTING

JING ZHANG

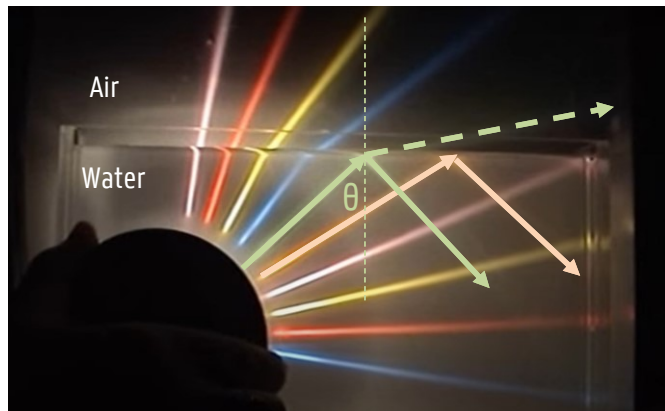
THE EXPLOSION OF SOCIAL MEDIA



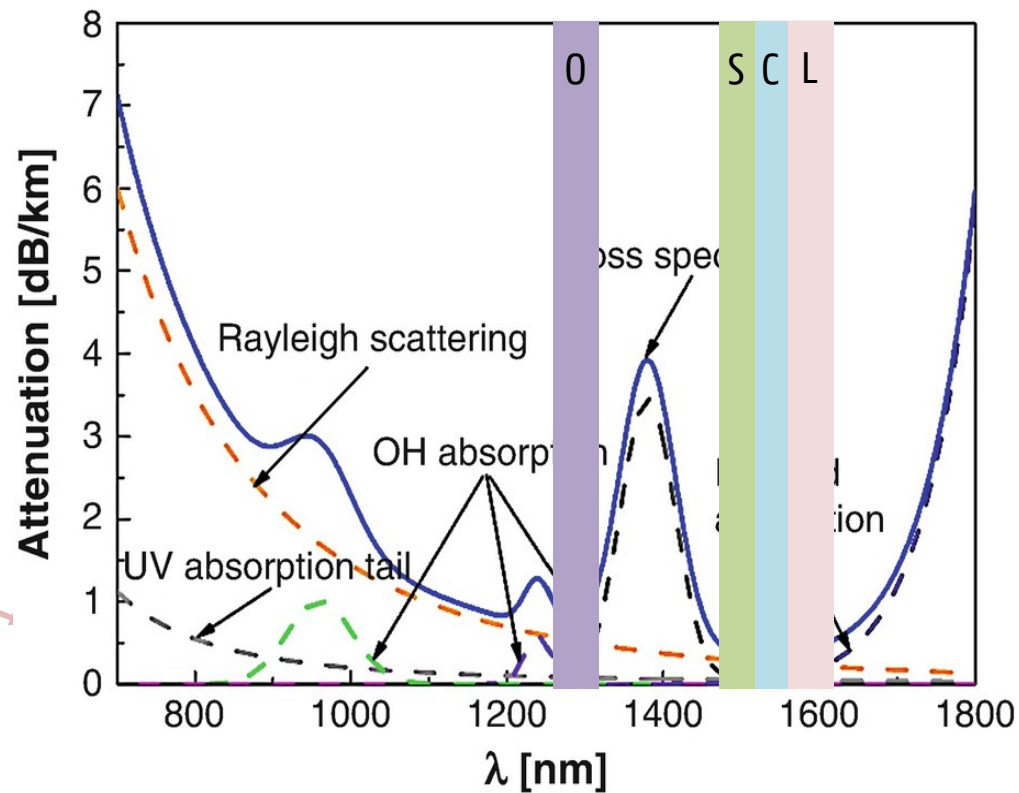
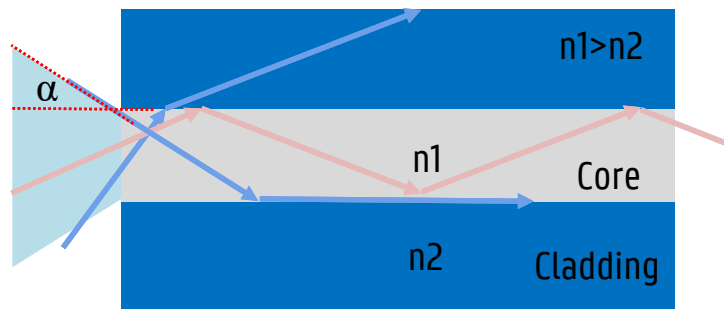
- Wireless networks access use
- Consumer devices use
- Data center use
- Fixed access wired use
- Production
- Fixed access WIFI use

Source: Andrae

KEY ENABLER: OPTICAL FIBERS

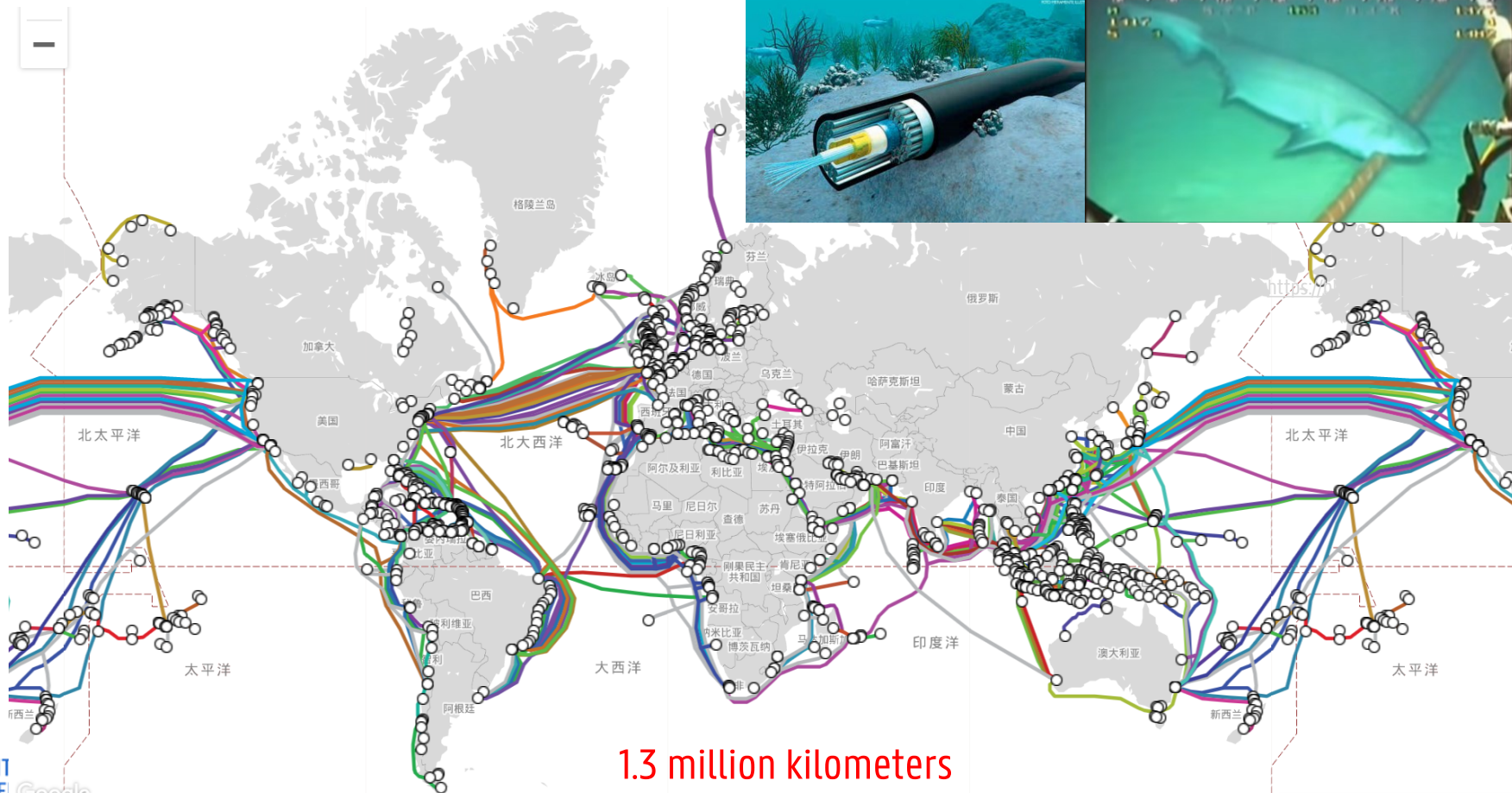


<https://www.youtube.com/watch?v=2kBOqfS0nmE>



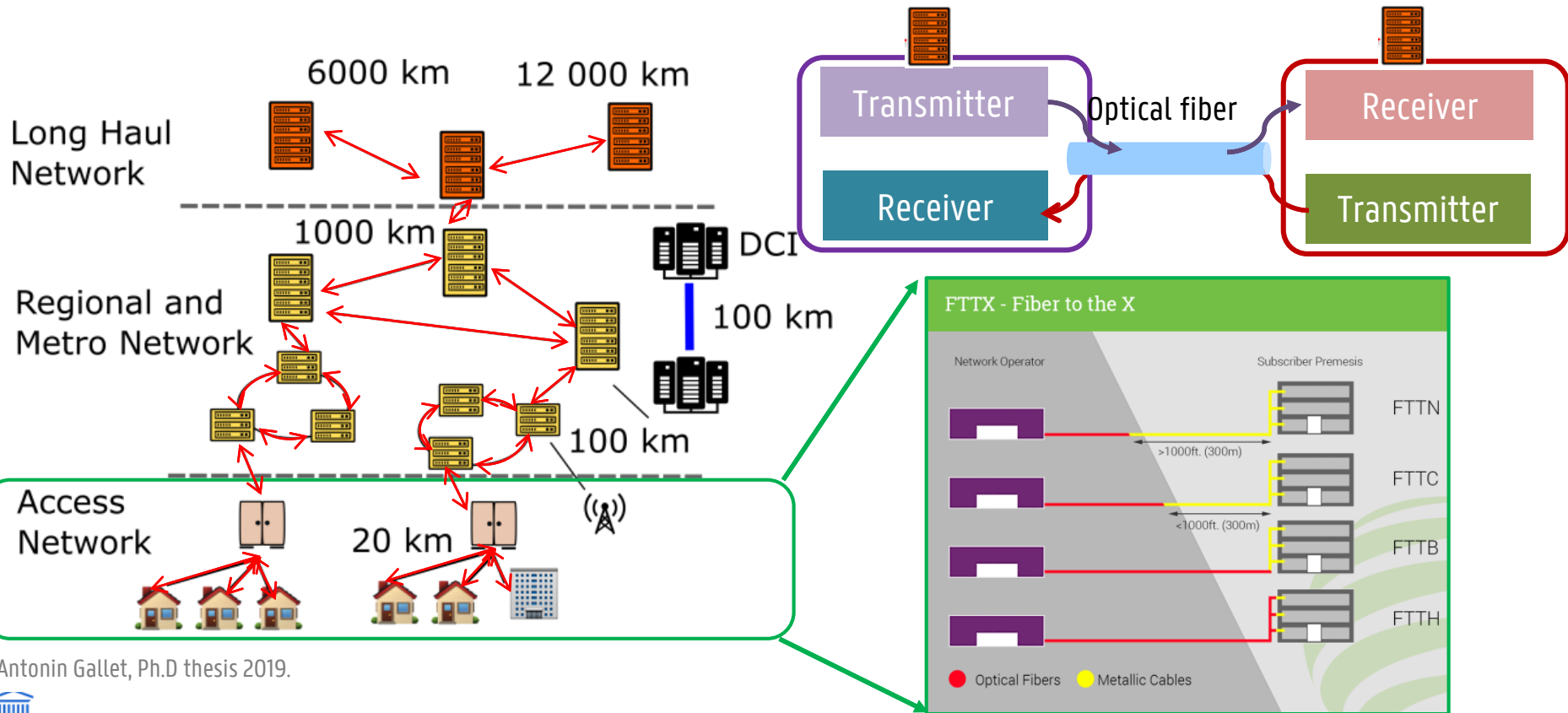
Source:technicasa

CONNECT THE WORLD



1.3 million kilometers

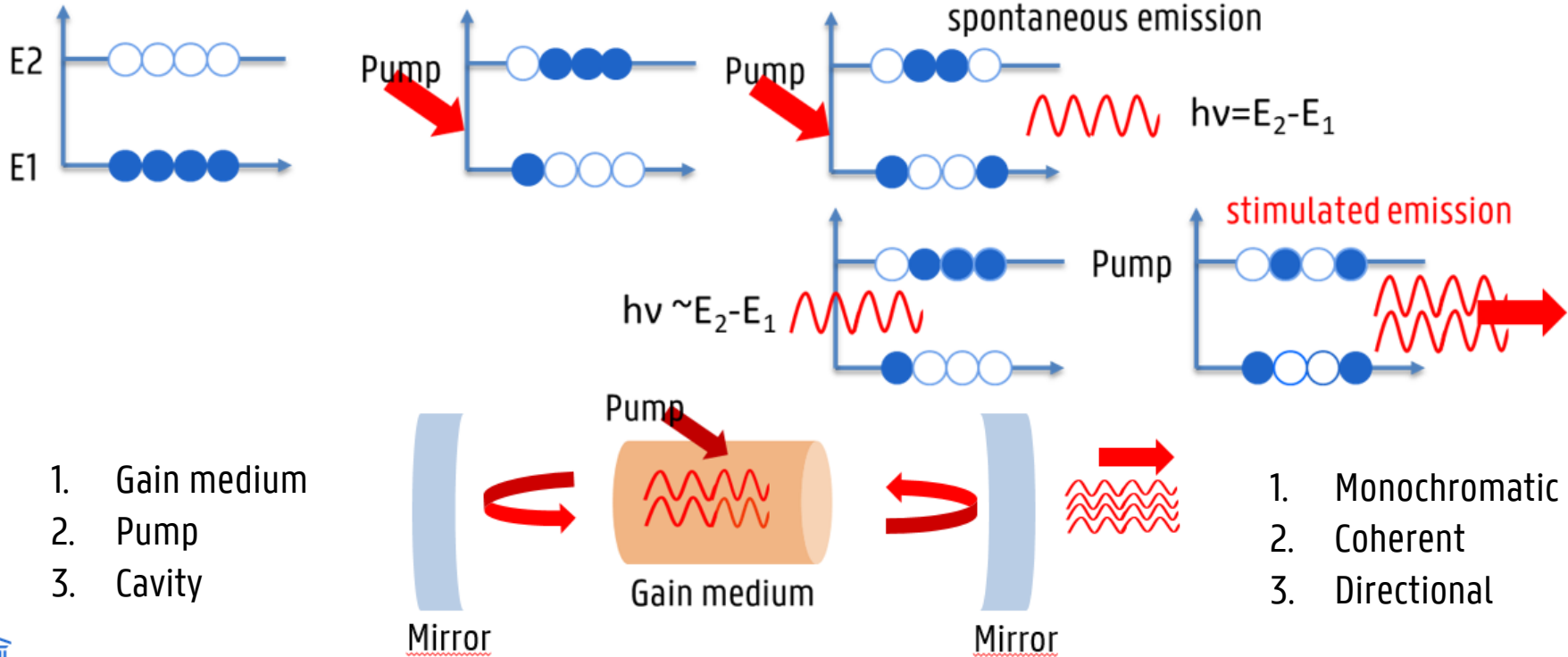
OPTICAL NETWORKS



Antonin Gallet, Ph.D thesis 2019.

OPTICAL SOURCE

LASER: Light Amplification by Stimulated Emission of Radiation

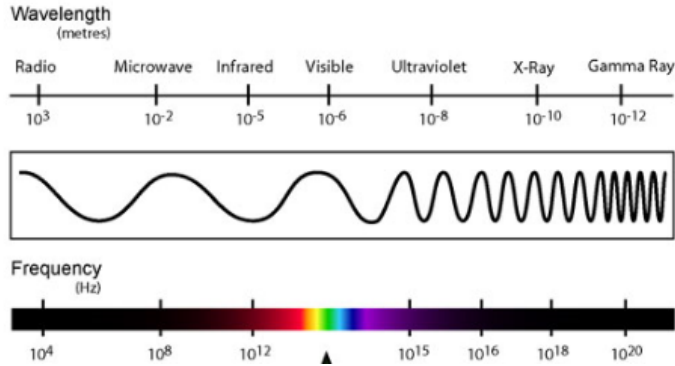


1. Gain medium
2. Pump
3. Cavity

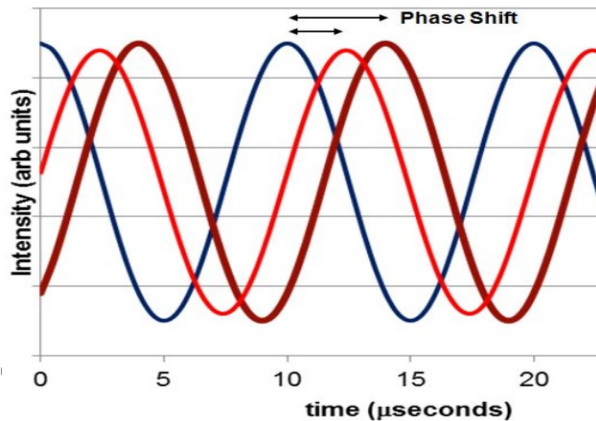
1. Monochromatic
2. Coherent
3. Directional

PROPERTIES OF LIGHT

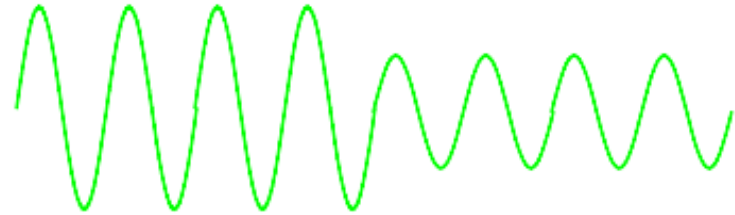
Wavelength(frequency)



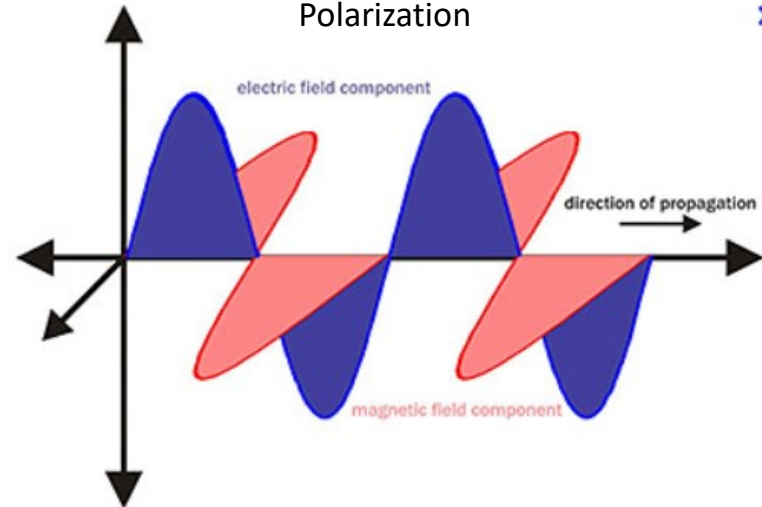
Phase



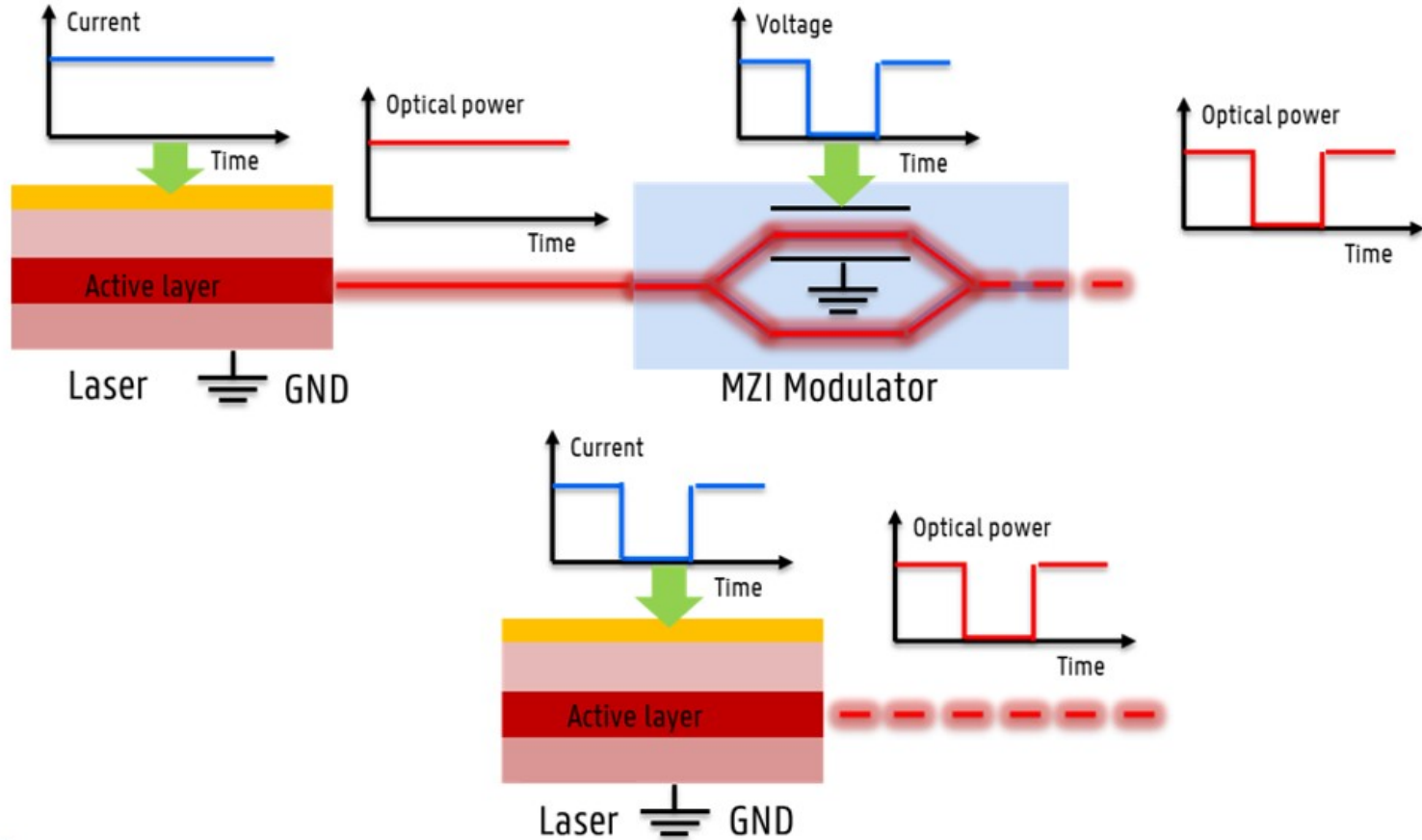
Amplitude (Intensity)



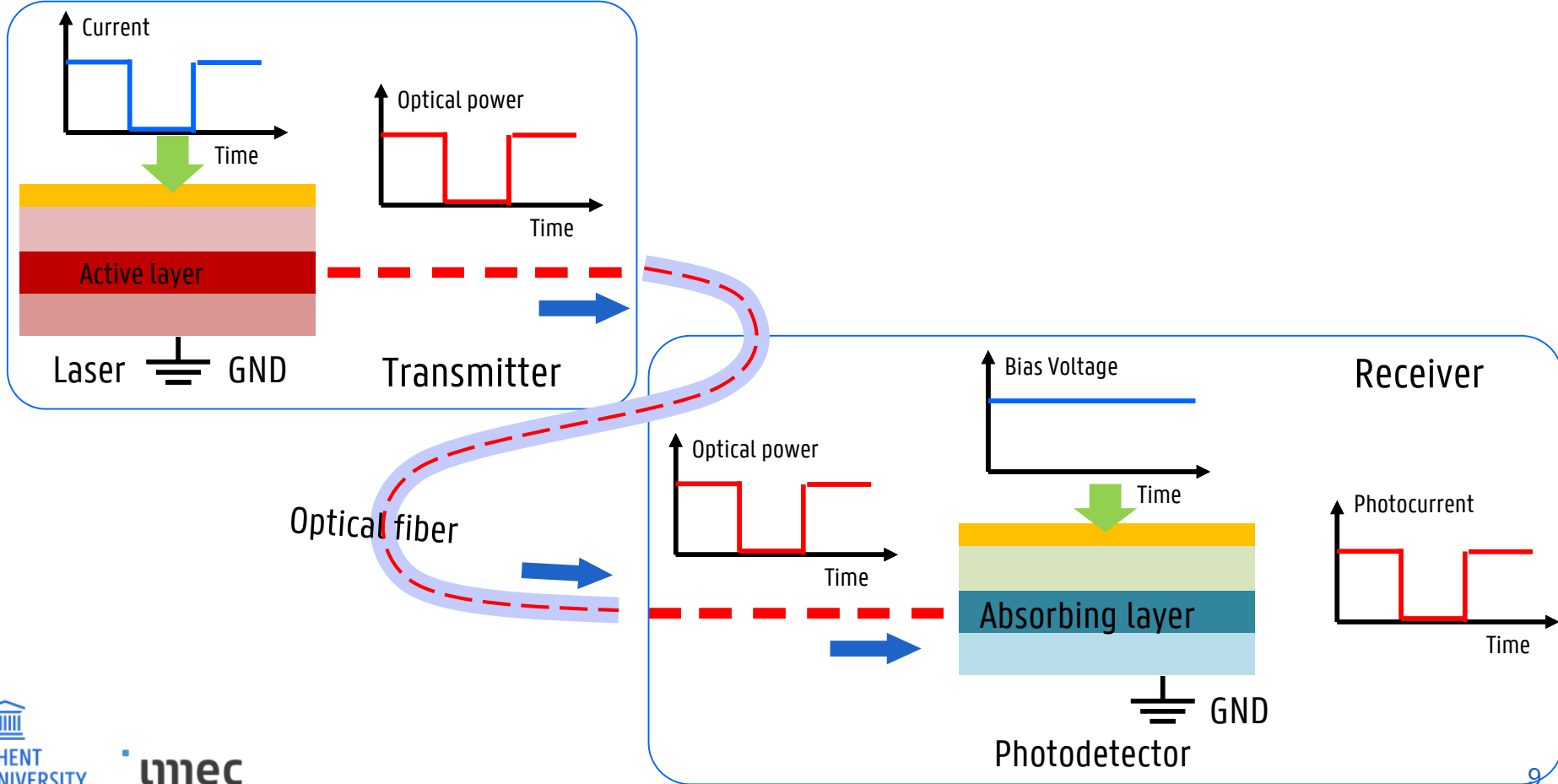
Polarization



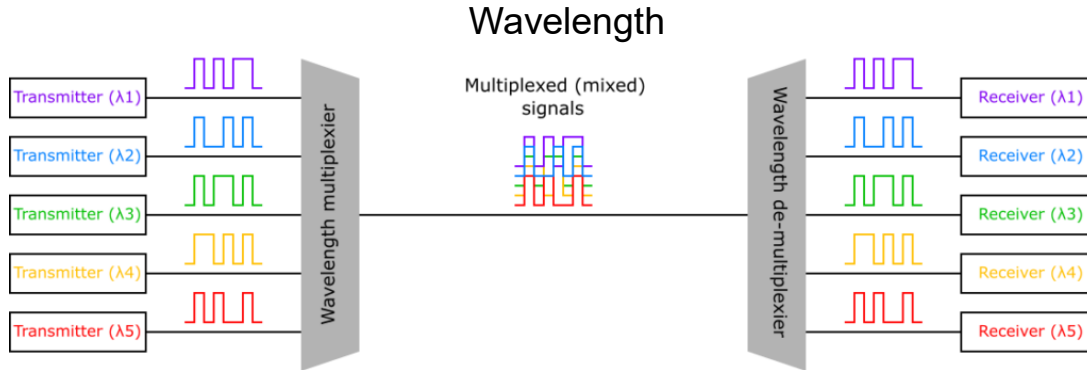
OPTICAL TRANSMITTER



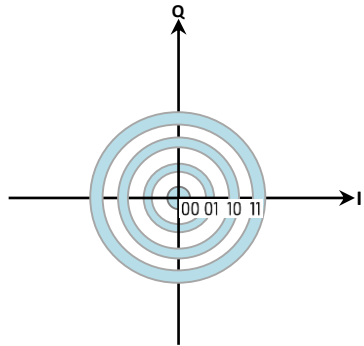
OPTICAL RECEIVER



SPEED UP THE NETWORK

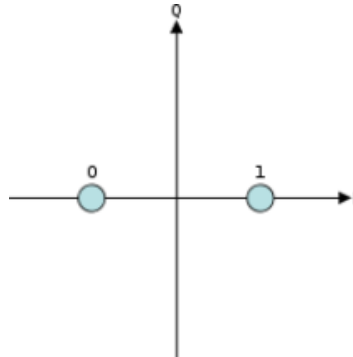


Amplitude/Intensity

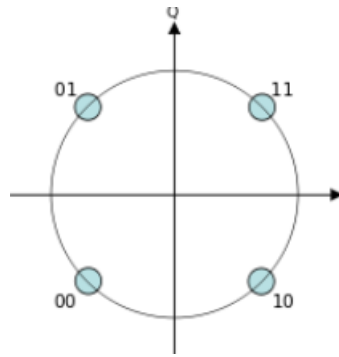


PAM4

Phase

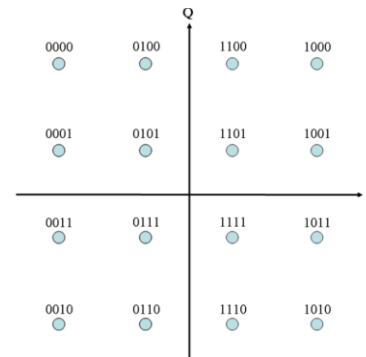


BPSK (Binary)



QPSK

Phase+amplitude

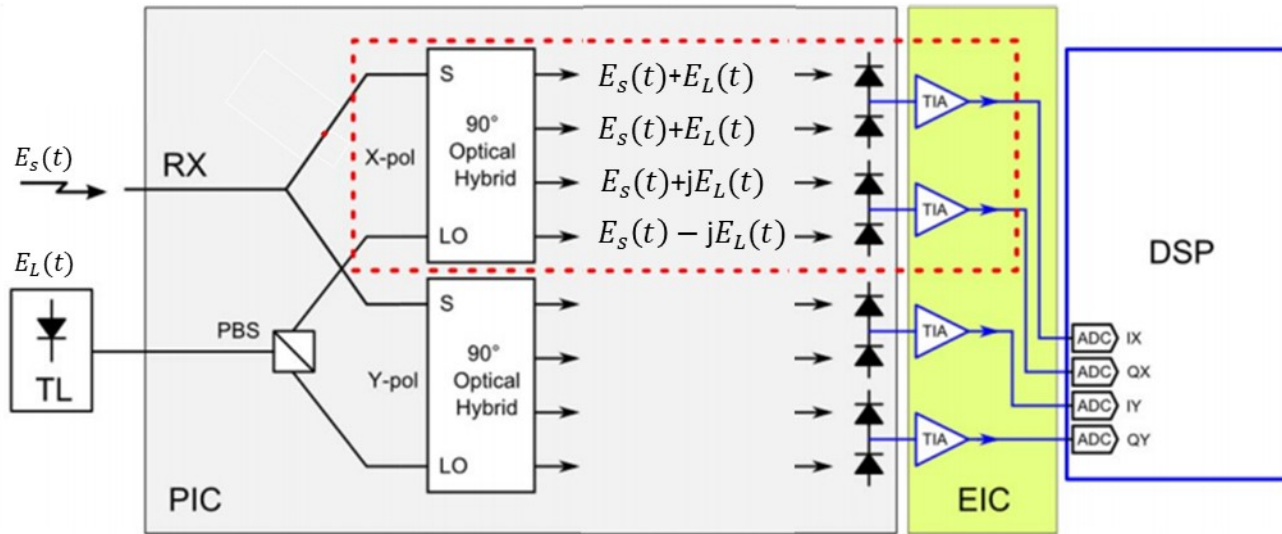


16-QAM

Phase
+
Amplitude
+
Polarization
...

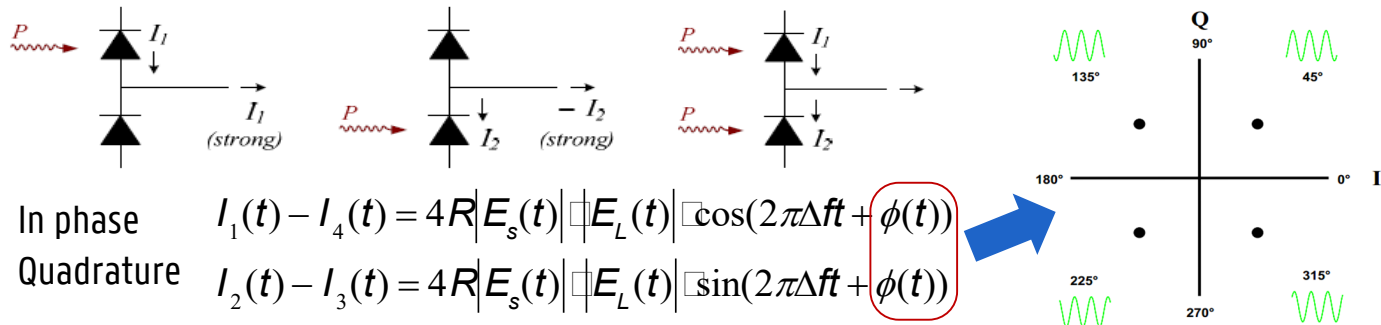
COHERENT RECEIVERS

1. Local oscillator (LO) laser—as a phase reference
2. 90° optical hybrid
3. Balanced detectors / Electronic circuits
4. Digital Signal Processor



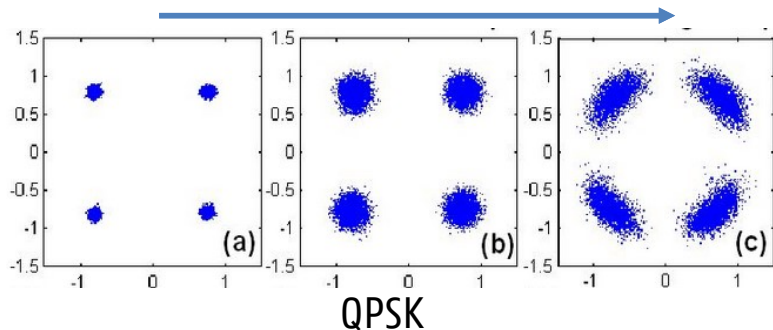
COHERENT RECEIVERS

Balanced detector

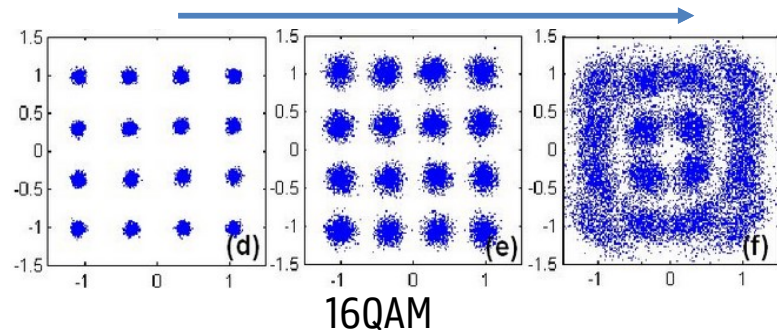


Local oscillator

Phase noise



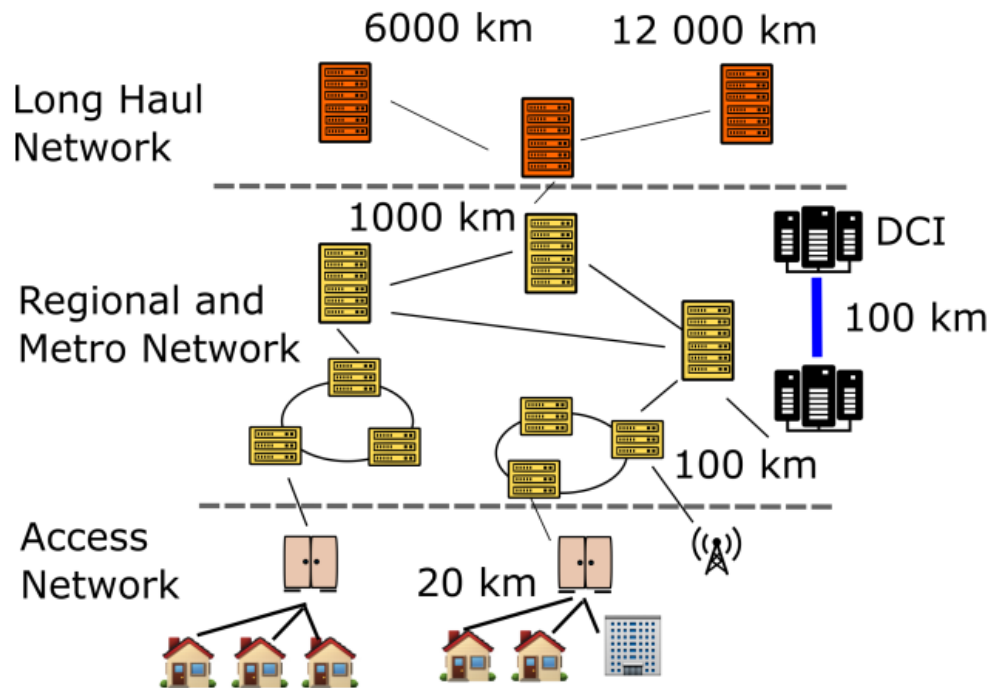
Phase noise



$t)$

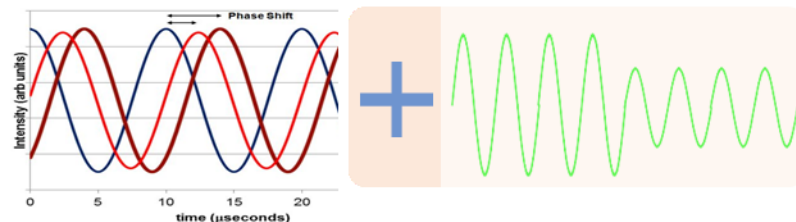
LO: low phase noise \rightarrow narrow linewidth [spec for 400ZR : 500kHz]

WHICH COMMUNICATION SYSTEM SHOULD BE USED?

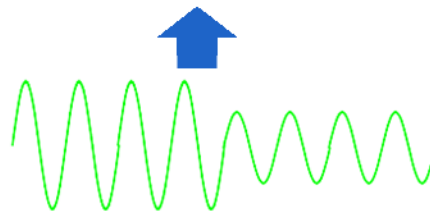


Antonin Gallet. Ph.D thesis 2019.

Coherent communication

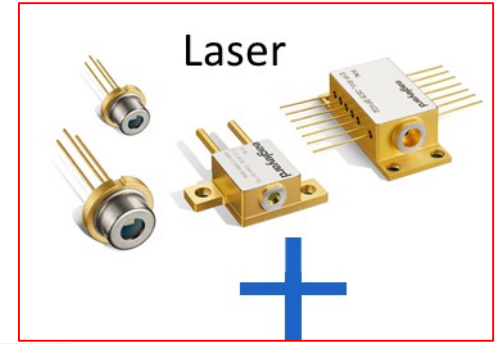
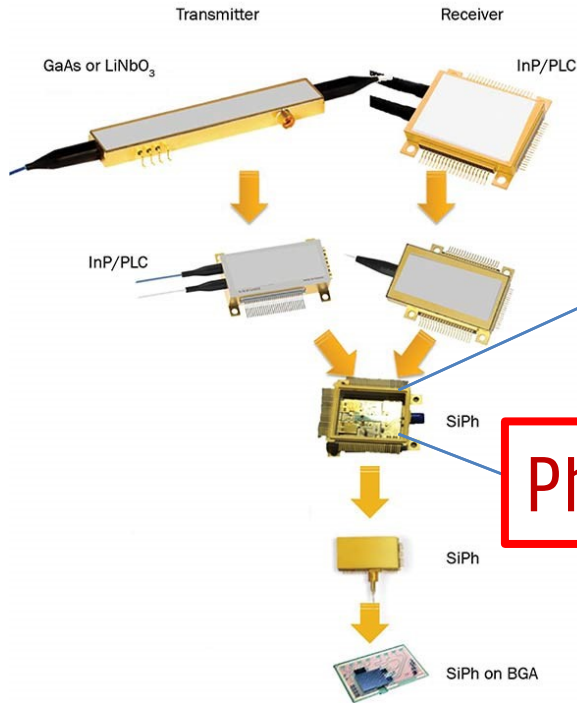


Cost & power consumption
VS
Performance

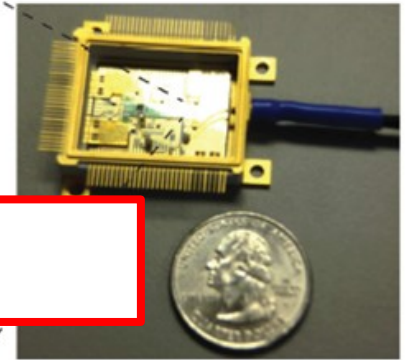
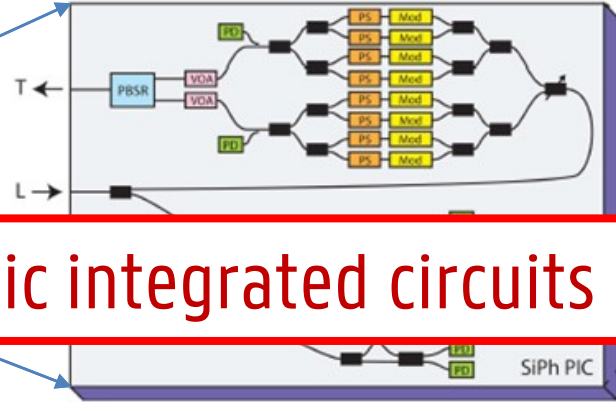


Intensity modulation and direct detection (IMDD)

SMALLER, CHEAPER, FASTER!

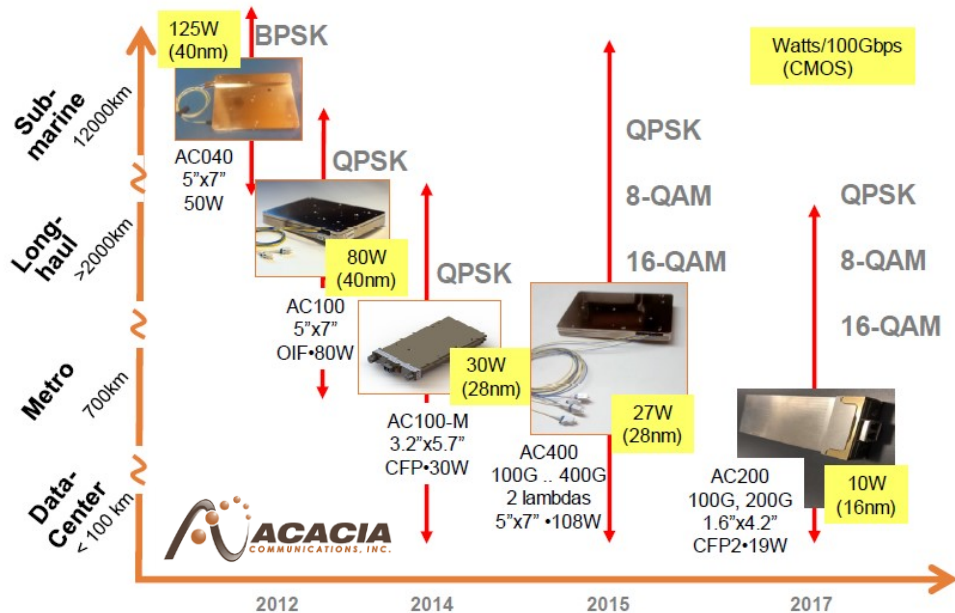


Photonic integrated circuits



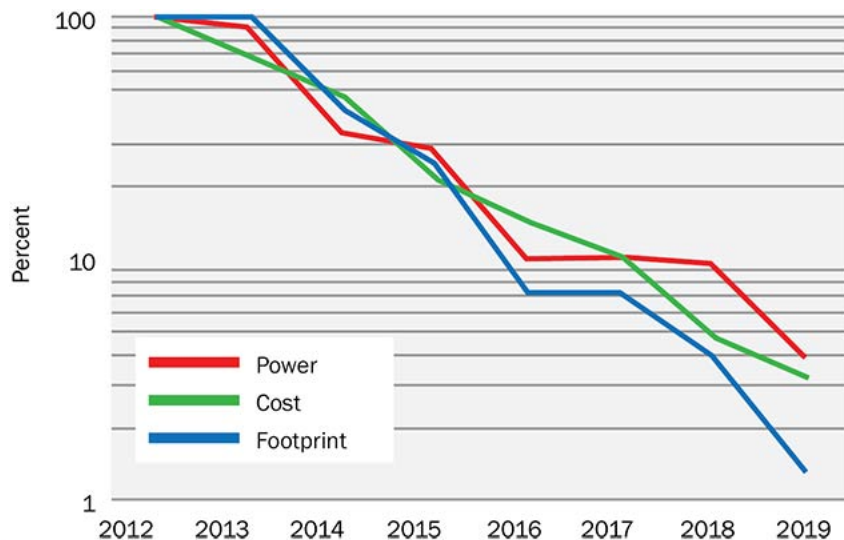
https://www.photonics.com/Articles/Trends_in_Silicon_Photonics_for_Fiber_Optic/p5/vo170/i1112/a64191

SMALLER, CHEAPER, FASTER!



Evolution in power and density per 100Gps for coherent transceiver modules

https://www.photonics.com/Articles/Trends_in_Silicon_Photonics_for_Fiber_Optic/p5/v0170/i112/a64191

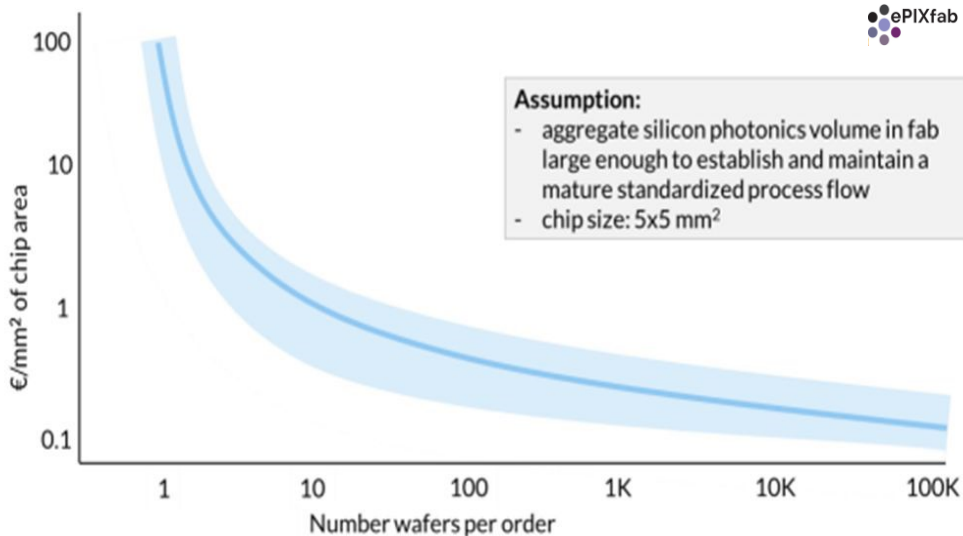


Power, cost, and footprint reduction in coherent communications per data transmission rate. Courtesy of Acacia Communications Inc

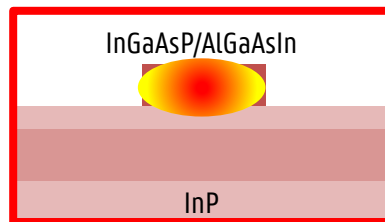
H. Zhang, and et. al., Opt. Express 26, 6943-6948 (2018)

PHOTONIC PLATFORMS

HOW MUCH A SILICON PHOTONICS CHIP COSTS?



Si

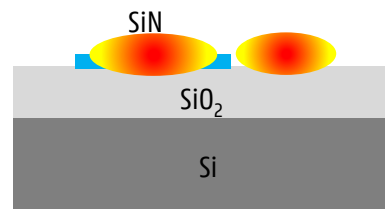


Passive+active functions

Expensive

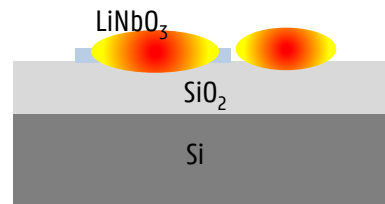
Medium yield

Small wafer



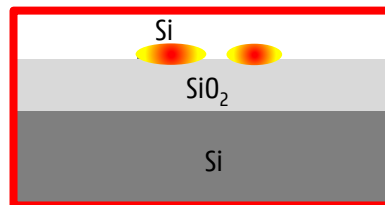
~0.5

~ 10 μm-1mm



~0.7

~10 μm



Cheap

High yield

Large wafer

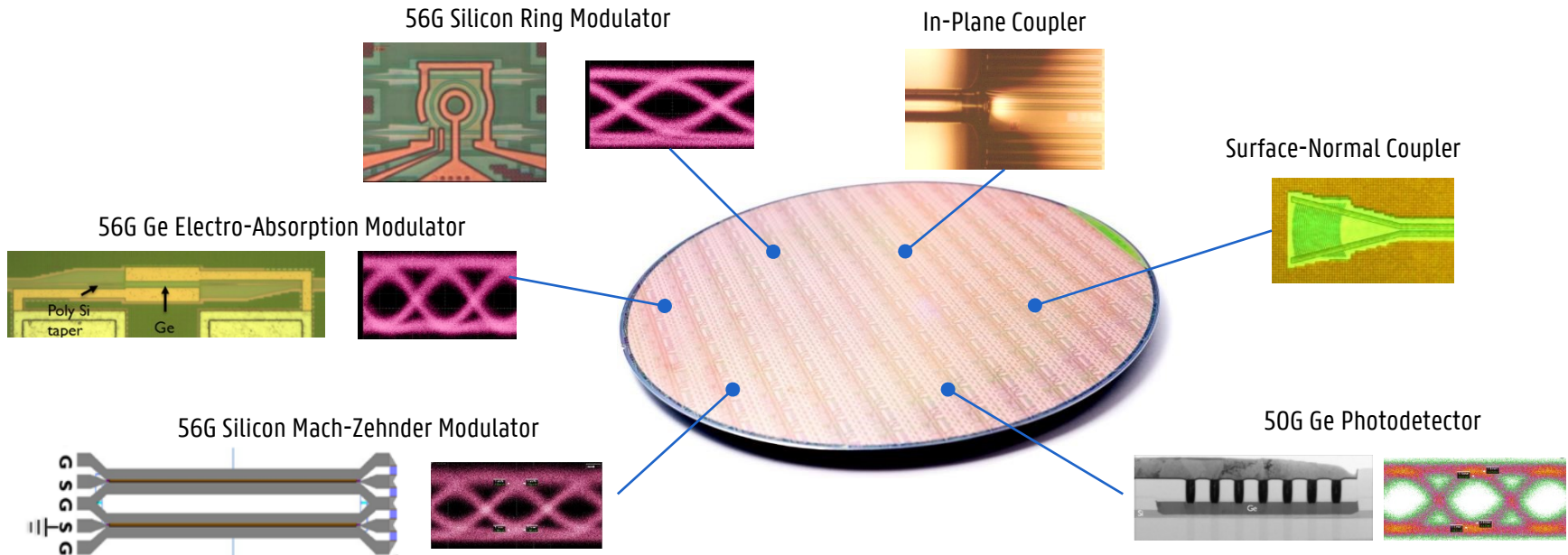
CMOS compatibility

Absence of lasers



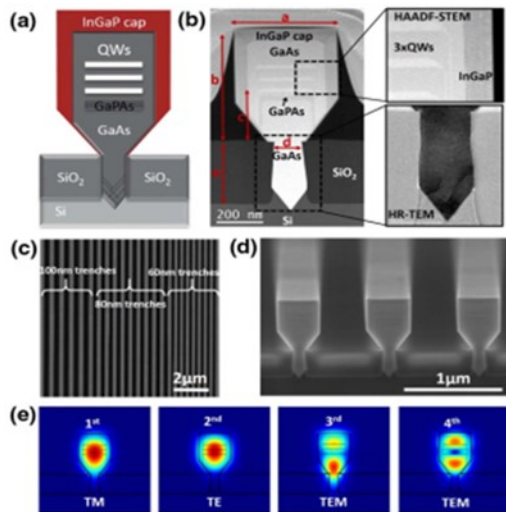
STANDING ON THE SHOULDERS OF GIANTS

The imec iSiPP50G platform

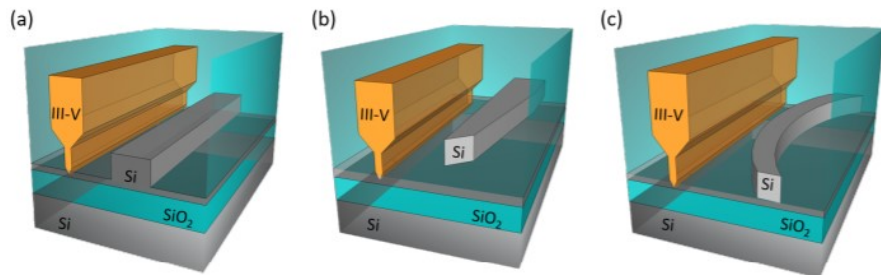


Co-integration of the various building blocks in a single platform
Today available on 200 mm wafer size, moving to 300mm
95% compatible with CMOS130 in commercial foundries

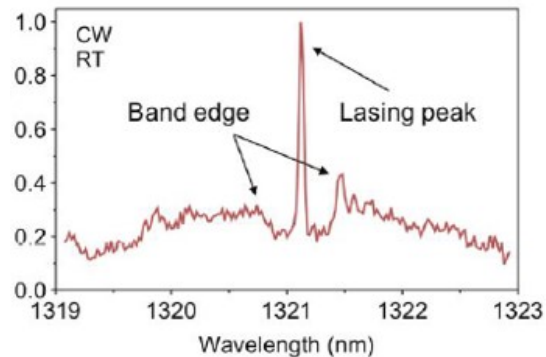
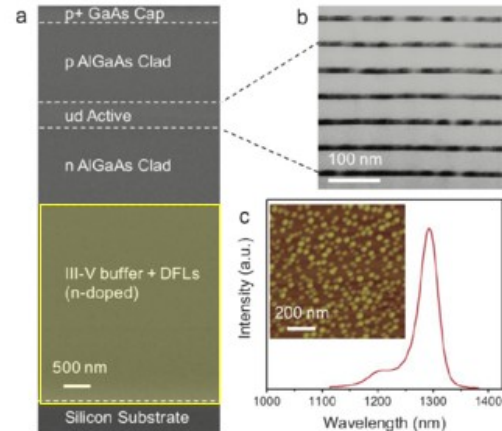
ULTIMATE SOLUTION—EPITAXIAL GROWTH OF III-V ON Si



Y. Shi, and et.al, *Optica* 4(12), p.1468, [2017]



Y. Shi, and et.al, 27,37781 *Optics Express* (2019)

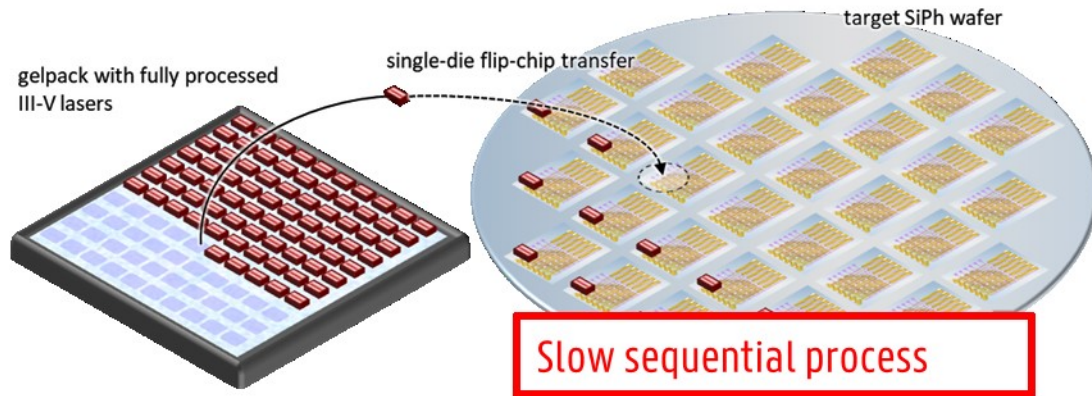


Yi Wang, and et.al, *Optica* 5, 528-533 (2018)

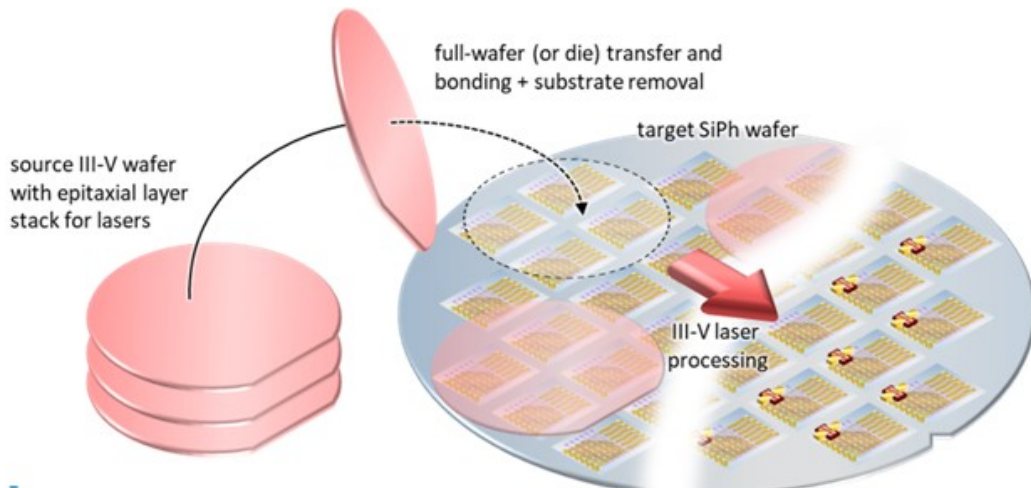
Still a lot of basic work to be done

ESTABLISHED III-V-ON-SILICO

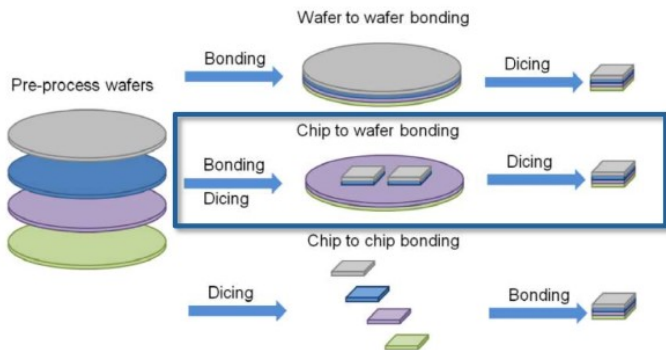
Flip-chip/pick-and-place integration



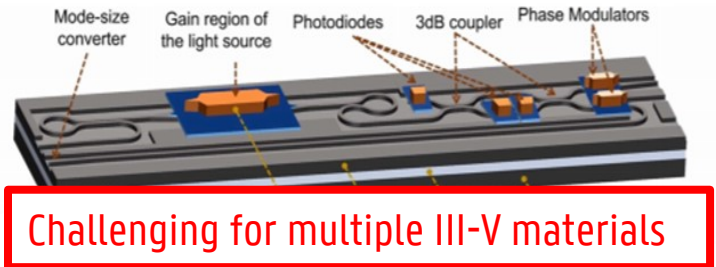
III-V/silicon wafer bonding



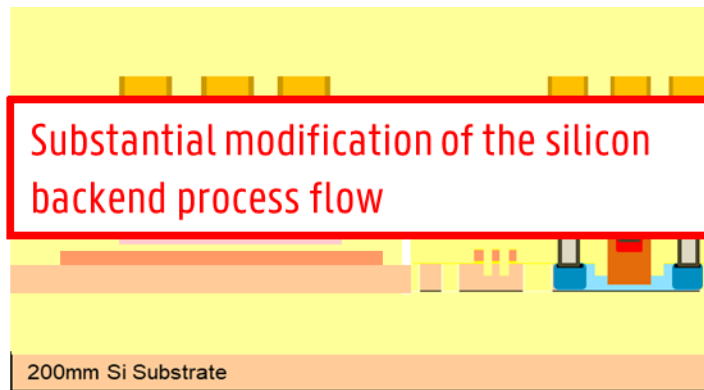
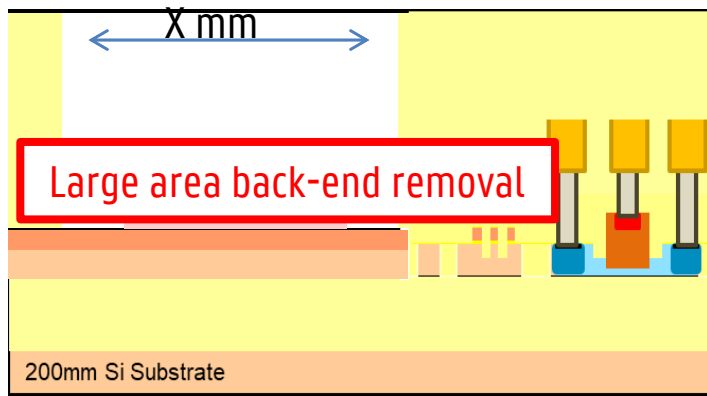
CHALLENGES FACED BY WAFER BONDING



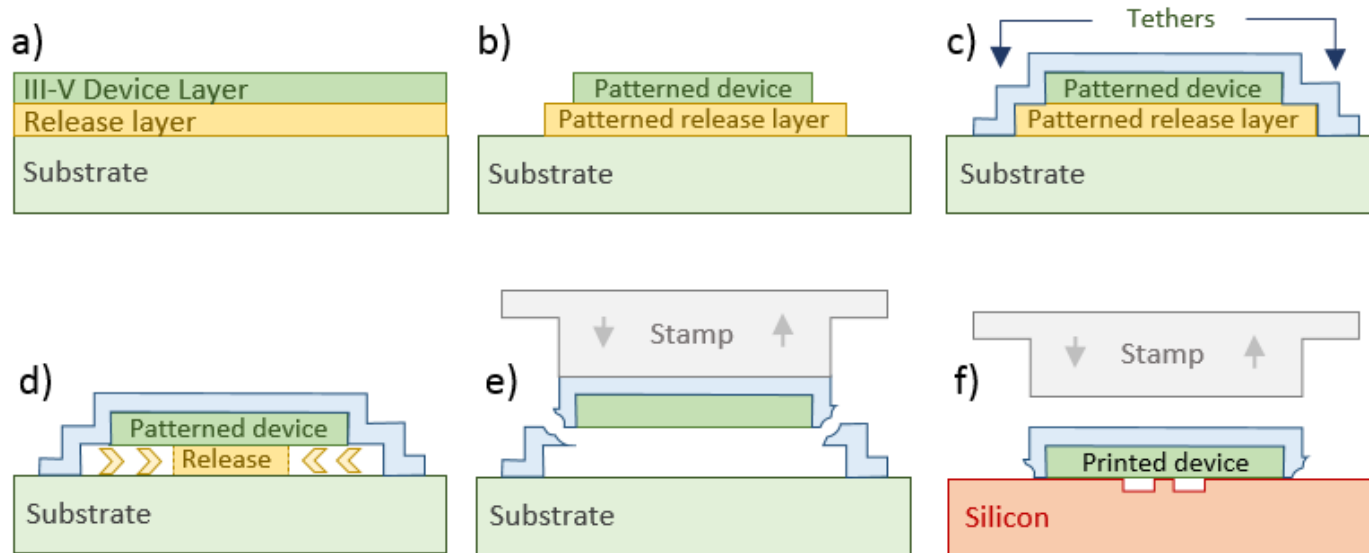
Source: Nanyang Technological University, Singapore



T. Komljenovic, et. al, Proceedings of the IEEE, vol. 106, no. 12, pp. 2246-2257, 2018



MICRO-TRANSFER PRINTING



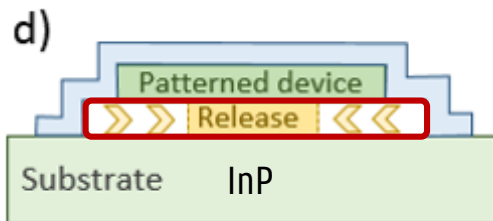
Transfer of released, micro-scale III-V devices to a Si target wafer

MICRO-TRANSFER PRINTING—CHOICE OF THE RELEASE LAYER

Candidate:

InAlAs

InGaAs

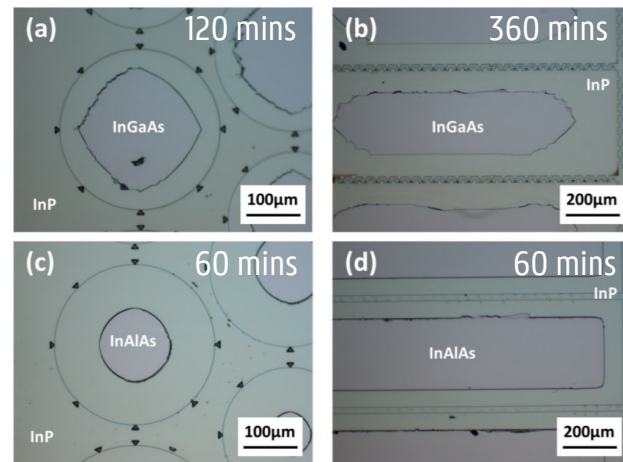
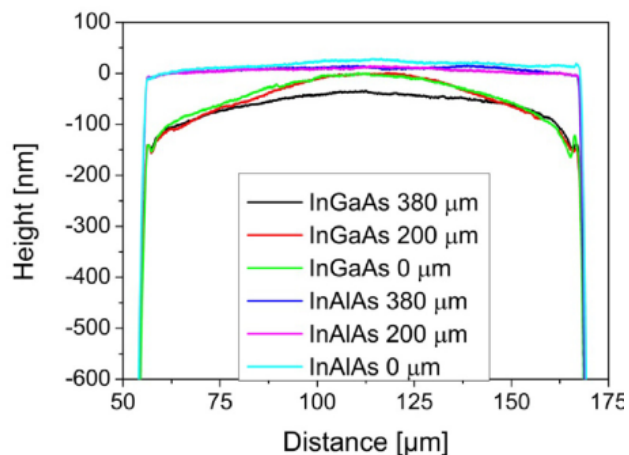
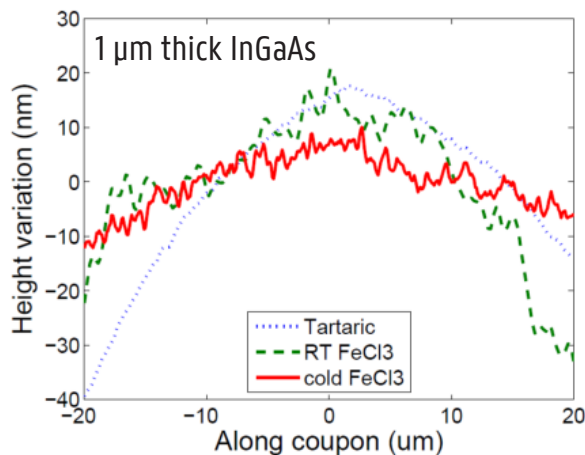


Selectivity to InP:

InAlAs: > 4,000

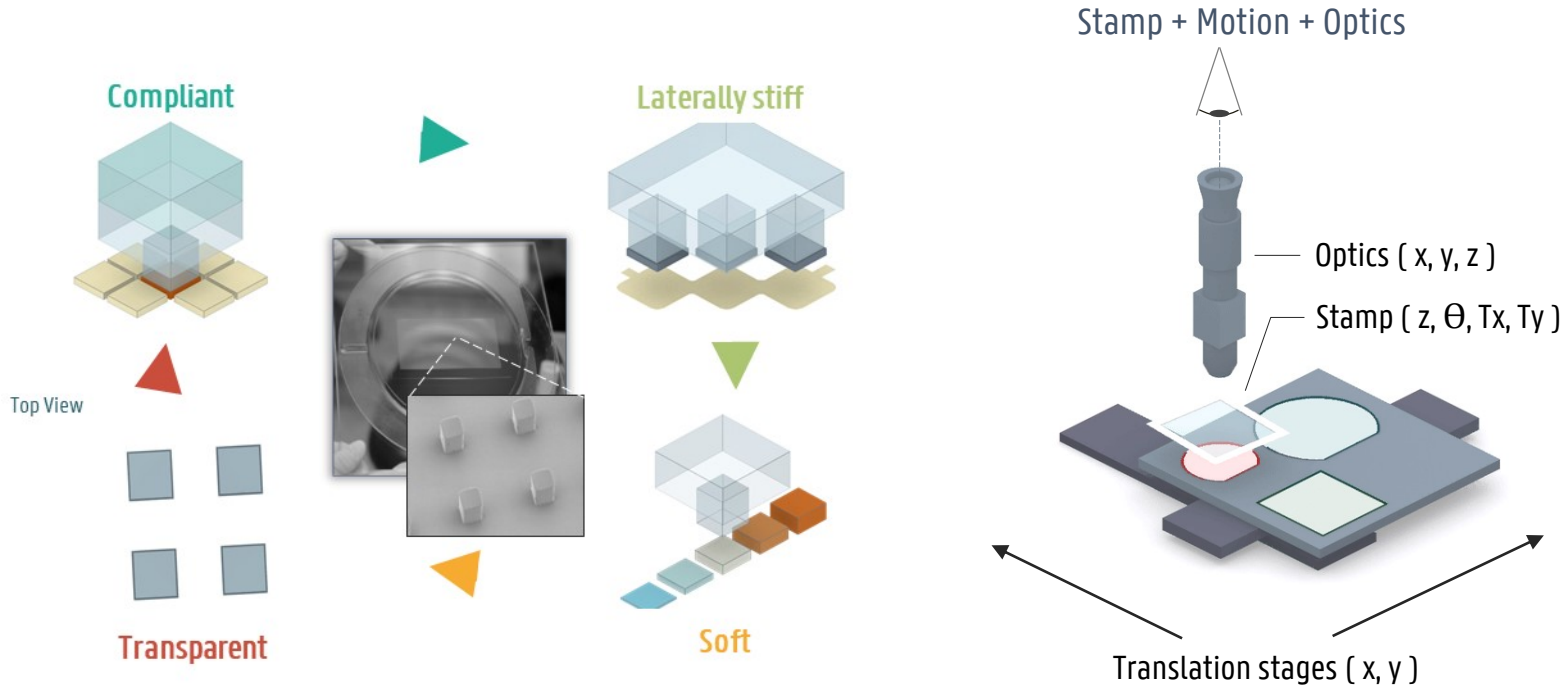
InGaAs: > 1,000

Etchant: cold $\text{FeCl}_3:\text{H}_2\text{O}$



J. O'Callaghan, 7(12) p.4408-4414, Optical Materials Express, 2017.

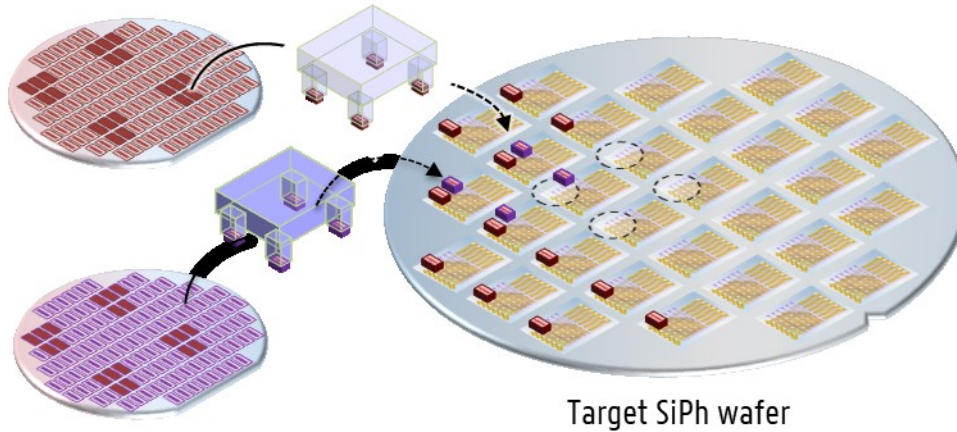
MICRO-TRANSFER PRINTING—STAMP AND TRANSFER PRINTING SYSTEM



Position tolerance of $\pm 1.5 \mu\text{m}$ at 3σ in large arrays
 $\pm 0.5 \mu\text{m}$ when printed in small arrays

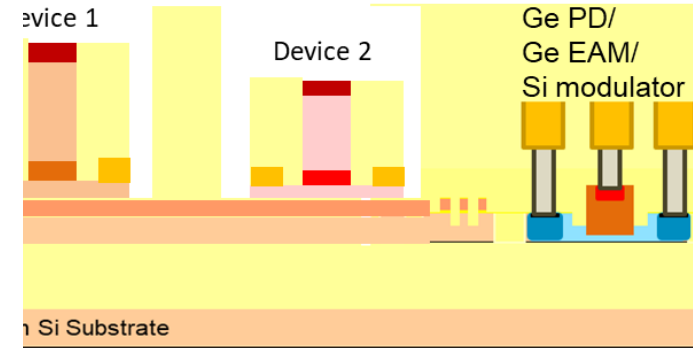
MICRO-TRANSFER PRINTING—INTEGRATION OF III-V/Si PICs

Simultaneous transfer of multiple coupons using elastomer stamp



Source III-V wafer with fully processed devices

10 μm



μTP combines advantages of flip-chip/pick-and-place and die-to-wafer bonding.

MICRO-TRANSFER PRINTING—SAVE EXPENSIVE MATERIALS

Transfer printing:

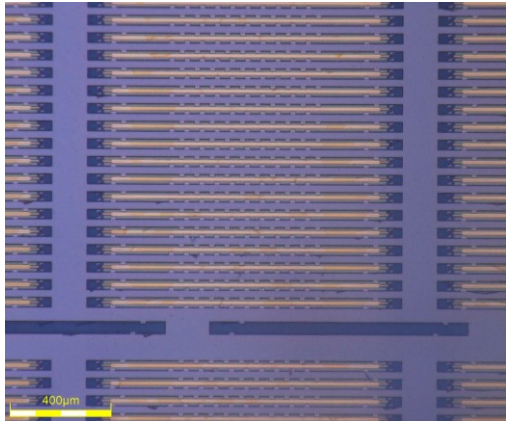
Coupon width: e.g. **50 μm**

Coupon length: e.g. 1.5 mm (device length $\sim 1.4\mu\text{m}$)

Pitch_y: **70 μm**

Pitch_x: ~ 1.6 mm

Potential of recycle and resut the thick substrate

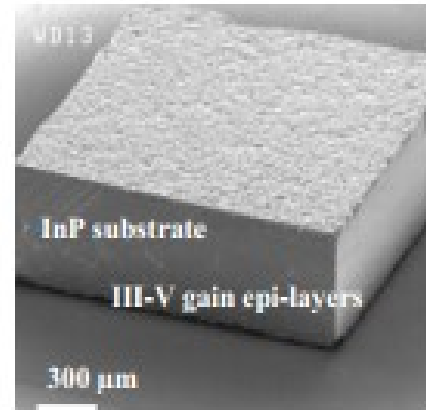


Pick-and-place:

Min III-V die size: 600 μm X **300 μm** (0201)

For a component with size of 1.4 mm X 50 μm

A 1.5 mm X **300 μm** III-V die is required



<https://www.manncorp.com/how-manufacturers-specify-equipment>

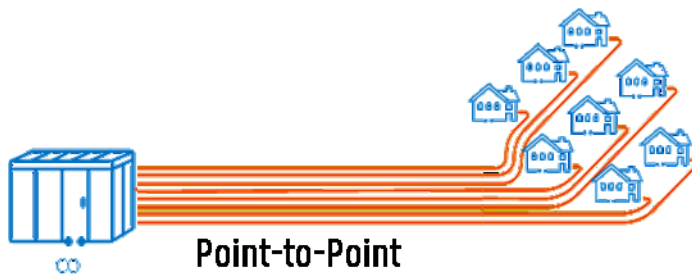
WHAT DID I DO IN MY PHD YEARS?

1. **Transfer-printed Fiber-To-The-Home (FTTH) transceivers**
 1. Transfer printing O-band photodiodes
 2. Transfer-printed DFB lasers
 3. **Four-channel point-to-point FTTH transceiver array**
 4. **A single-channel point-to-point FTTH transceiver based on the co-integration of DFB laser and O-band PD**
2. **Integration of III-V-on-Si coherent receivers through micro-transfer printing**
 1. **Transfer-printed widely tunable and narrow linewidth laser**
 2. Transfer-printed coherent receiver on a passive PIC
 3. **Integrated coherent receiver based on the imec iSiPP25/50G platform**

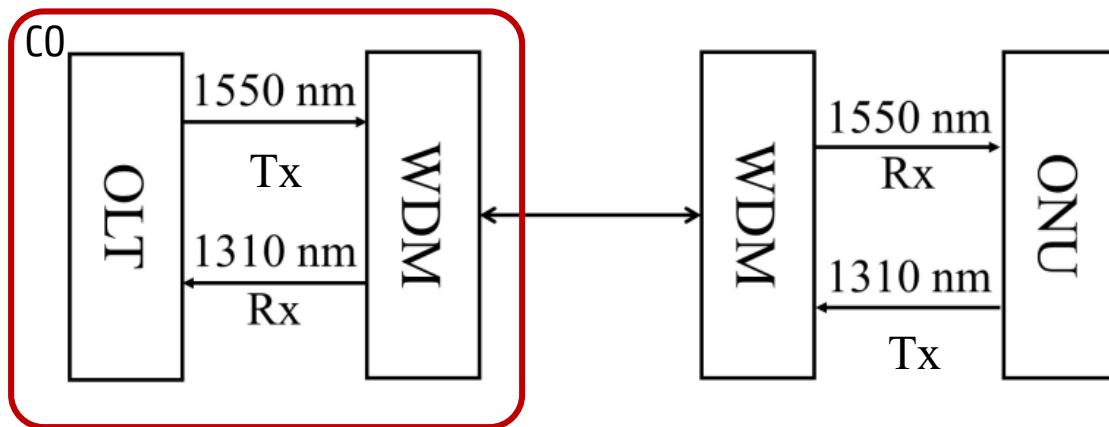
WHAT DID I DO IN MY PHD YEARS?

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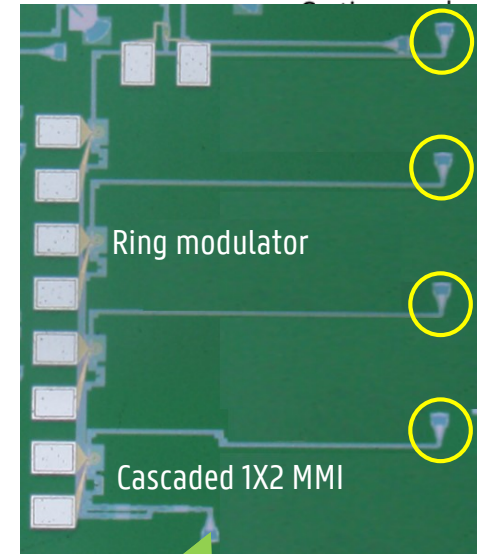
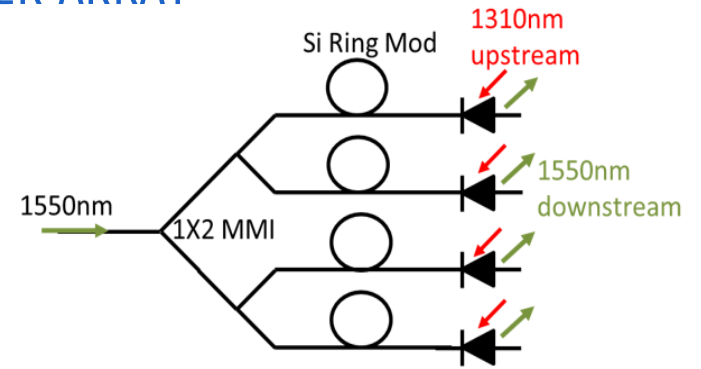
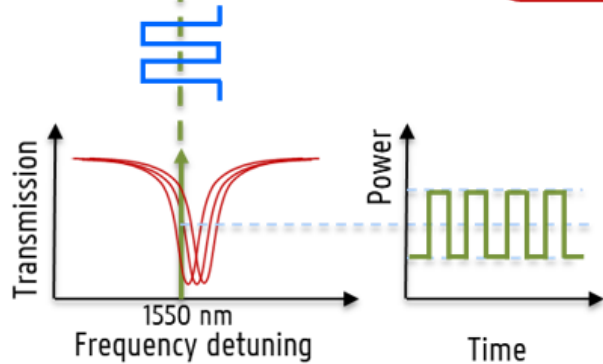
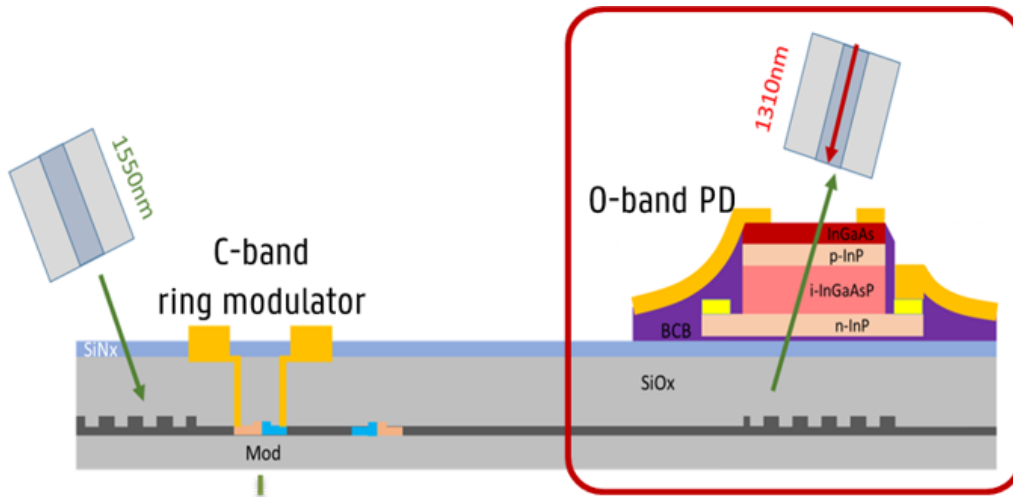
FIBER-TO-THE-HOME



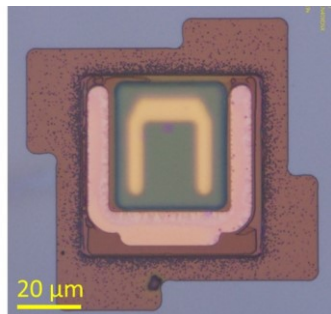
- Higher bandwidth
- Good service isolation
- Good security
- Long reach
- Dedicated fiber line for each end user
- Large number of transceivers in the CO



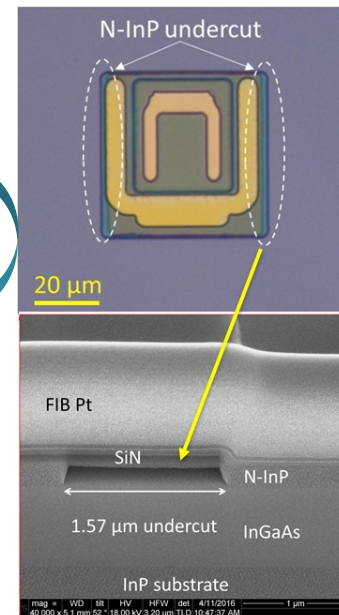
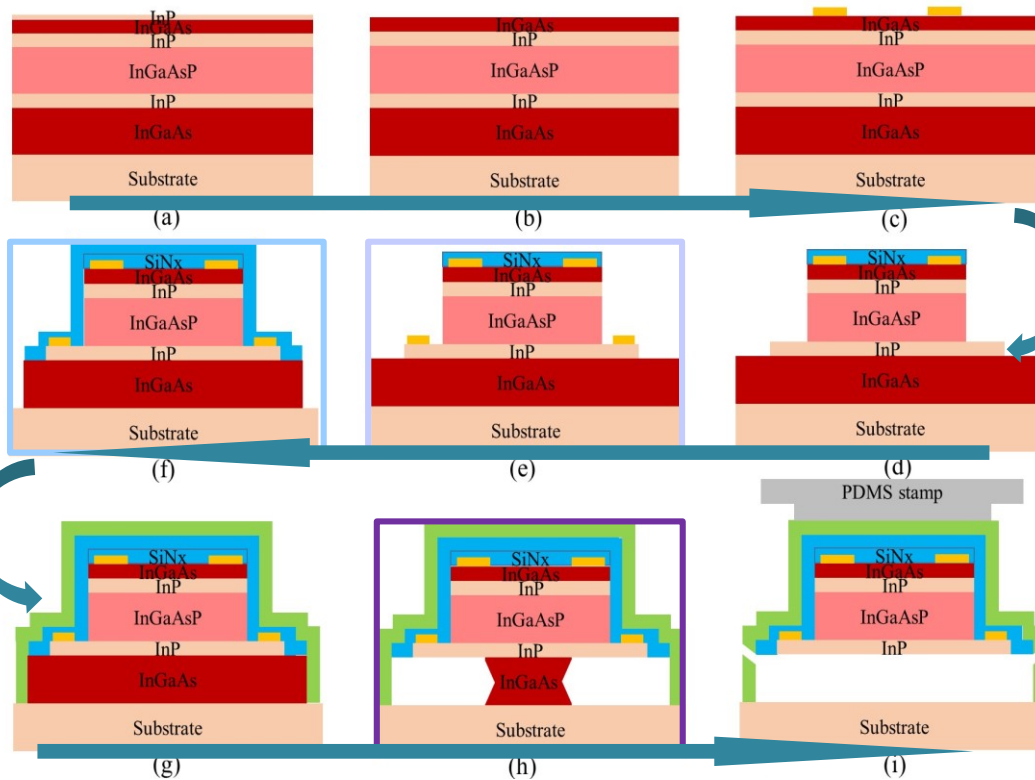
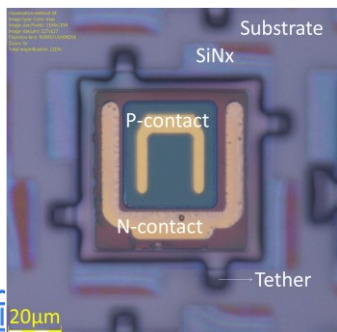
FOUR-CHANNEL POINT-TO-POINT FTTH TRANSCEIVER ARRAY



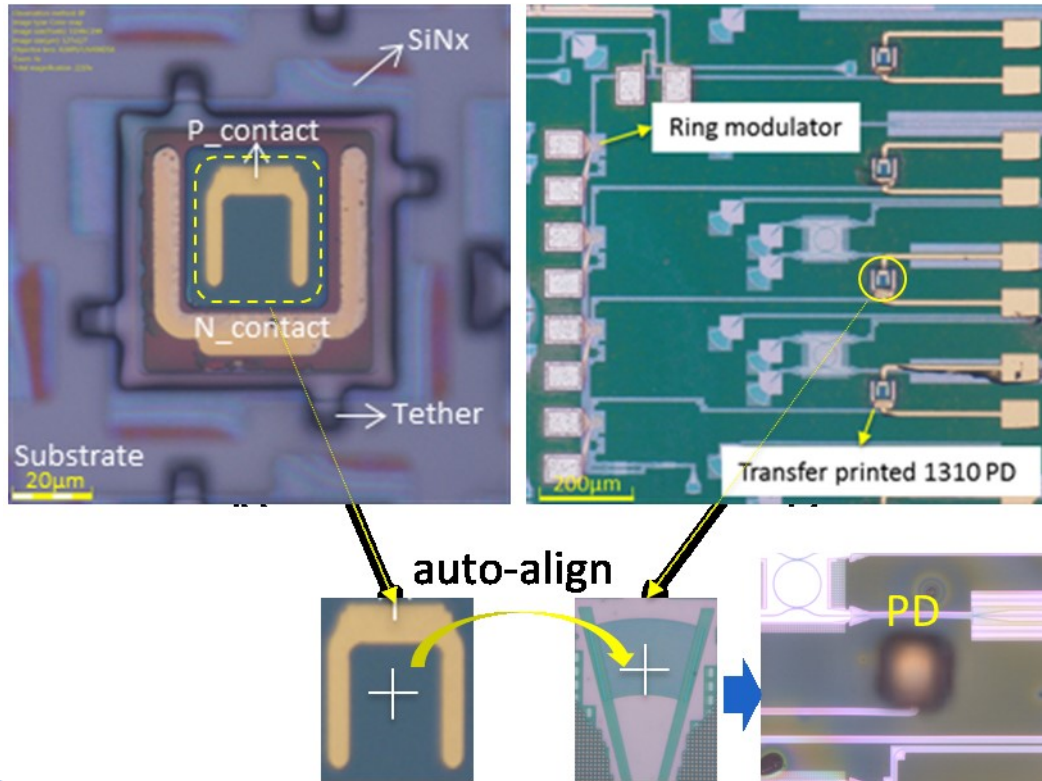
PRE-FABRICATION OF O-BAND PDs ON THE SOURCE WAFER



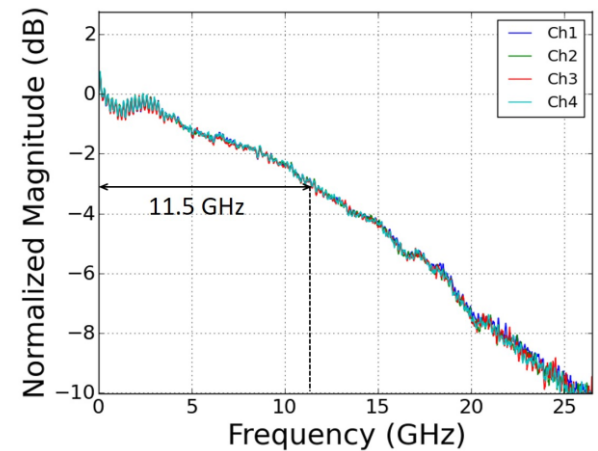
1 μm InGaAs release layer
Release: 100 mins
Etchant: FeCl₃:H₂O @5°C



TRANSFER PRINT PRE-FABRICATED O-BAND PDs ON AN ISIPP25G CHIP



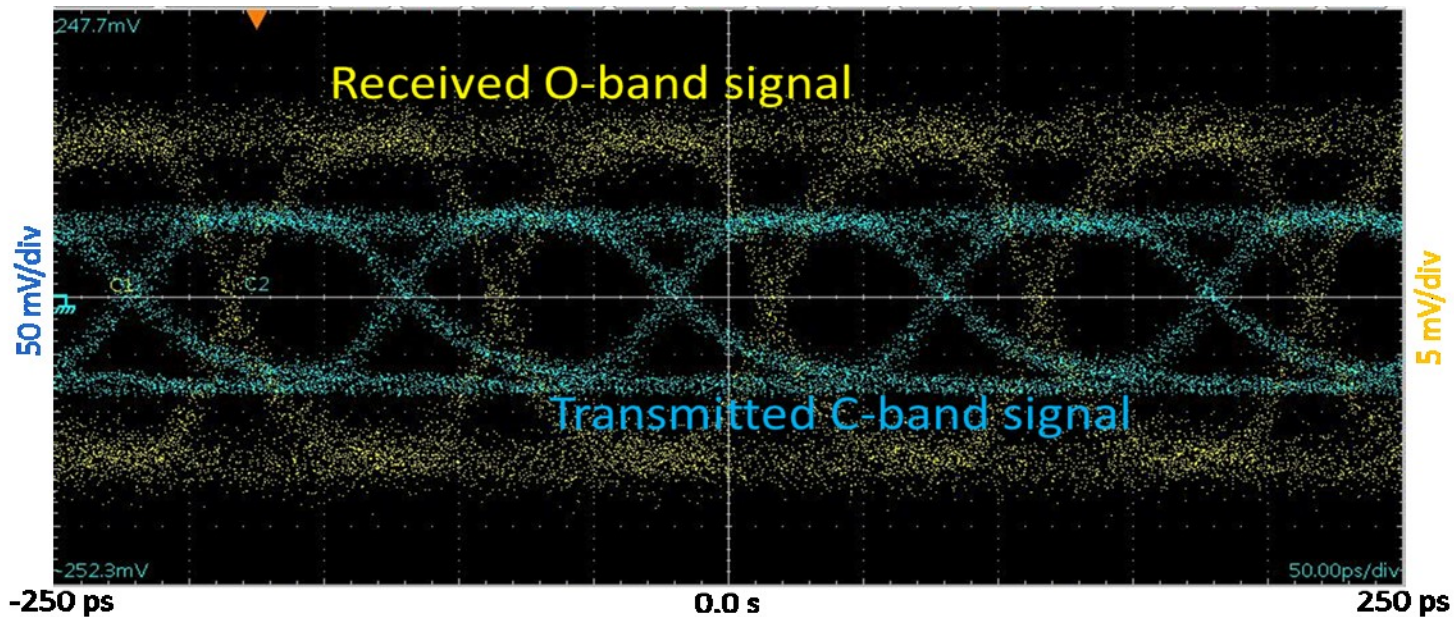
Responsivity in O-band : ~ 0.4 A/W
Responsivity in C-band : 0.025-0.03 mA/W



DEMONSTRATION OF THE TRANSCEIVER ARRAY

Signal format : NRZ-PRBS

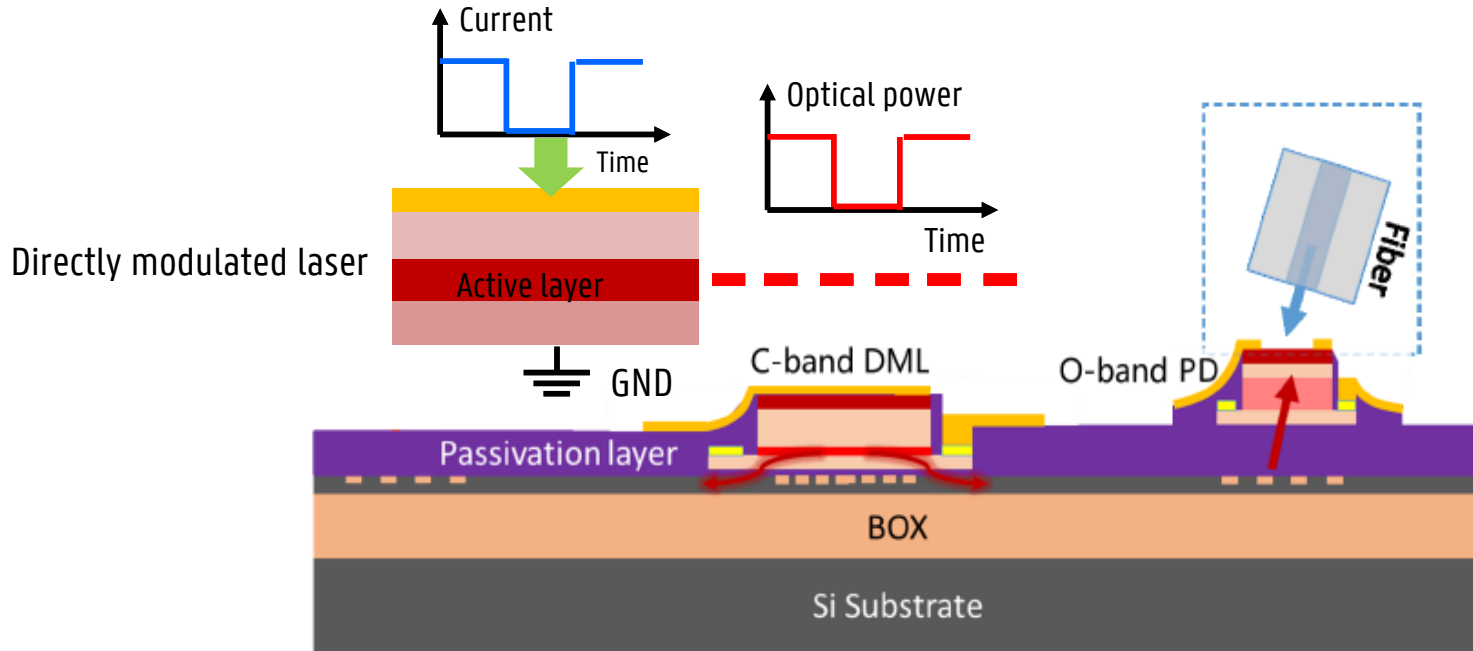
Error-free operation demonstrated



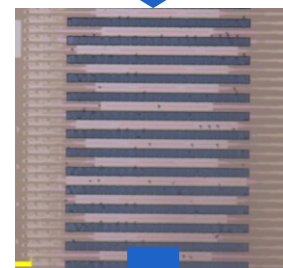
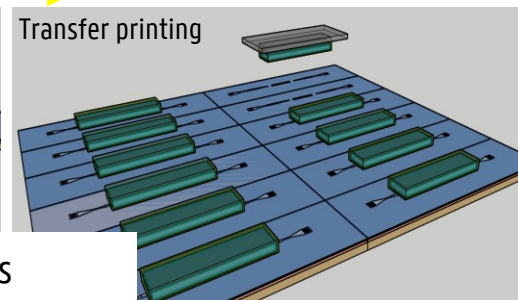
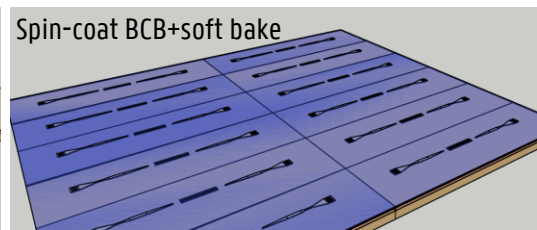
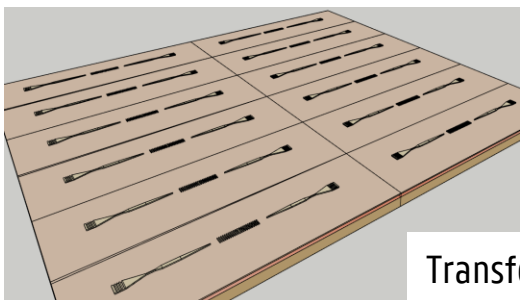
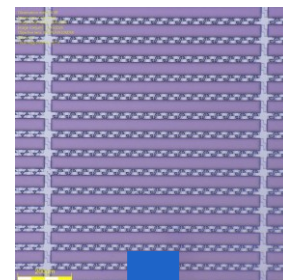
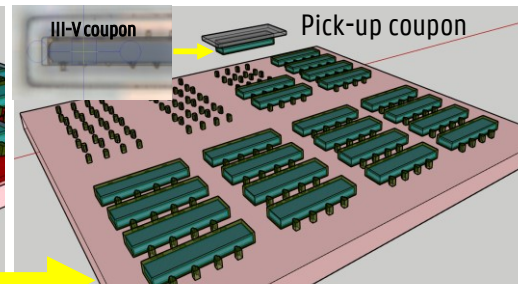
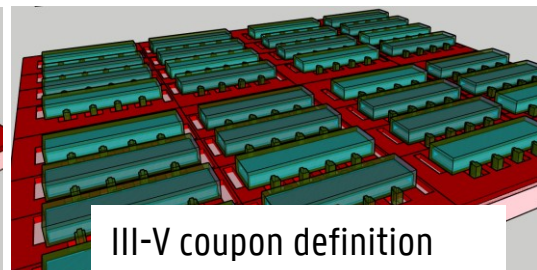
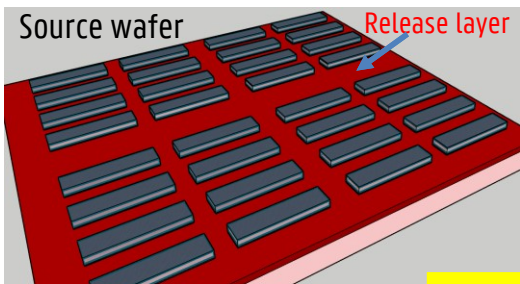
SINGLE CHANNEL FTTH TRANSCEIVER

Co-integration of a DFB laser (transmitter) and an O-band PD(receiver) on a passive PIC

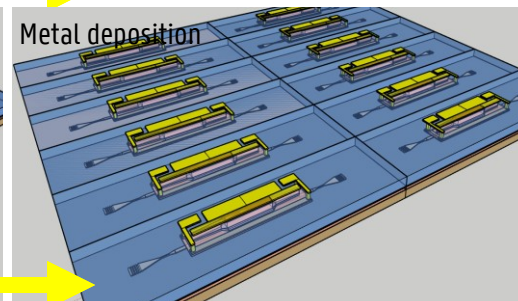
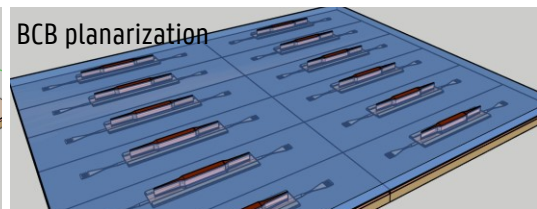
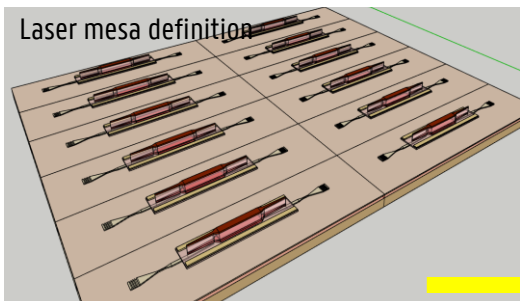
Si waveguide circuit: 400 nm thick, 180 nm single-step etch.



MICRO-TRANSFER-PRINTED III-V-ON-SI DFB LASERS



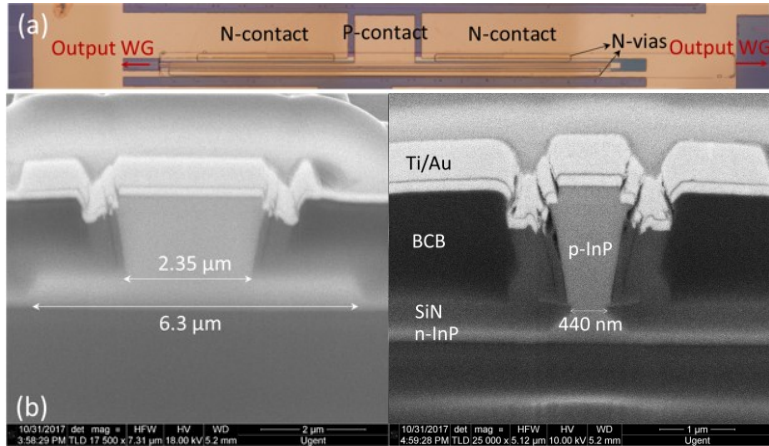
Transfer print III-V coupons on target Si PICs



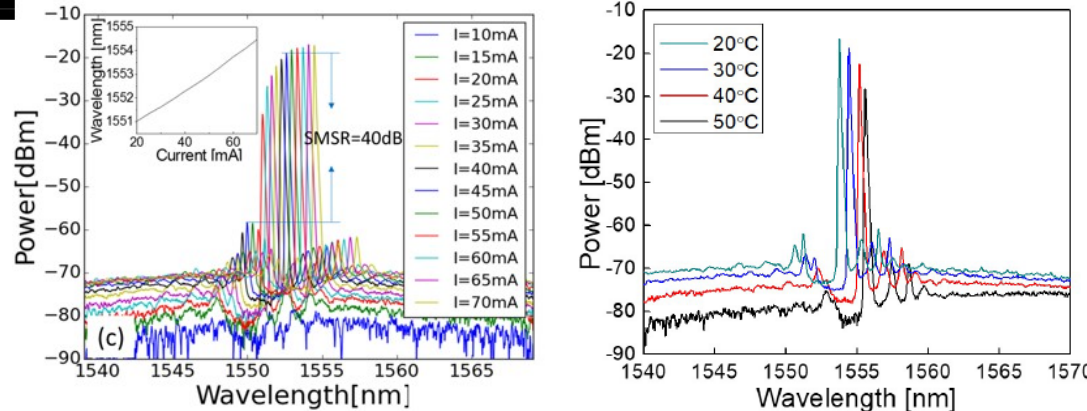
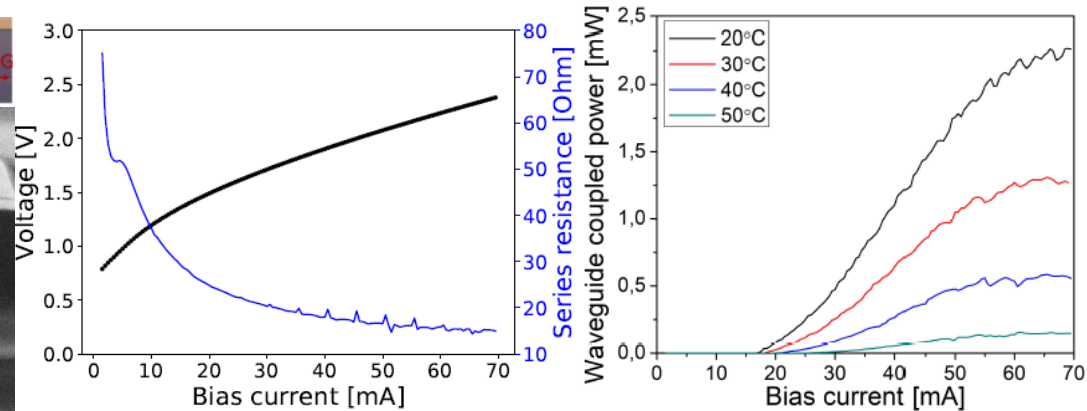
Post-printing processes



MICRO-TRANSFER-PRINTED III-V-ON-SI DFB LASERS—2ND GENERATION



- Grating period: 477 nm(2nd order)
- Grating length: 300 μm
- Lateral misalignment: 240 nm
- $KL= 5.16$
- SMSR: >40 dB
- Threshold: 18 mA
- Waveguide coupled power: 2 mW



SINGLE CHANNEL FTTH TRANSCEIVER

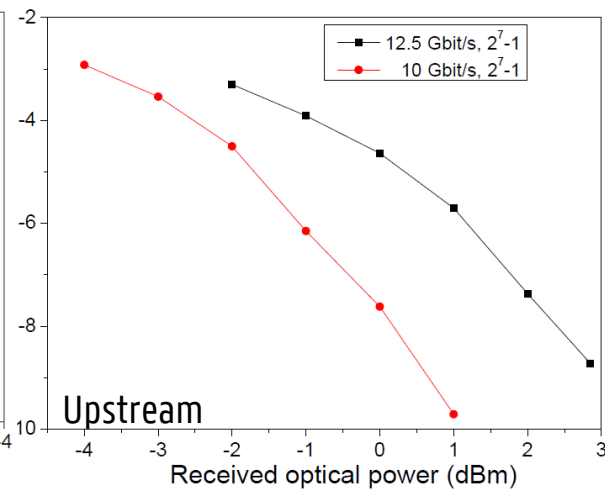
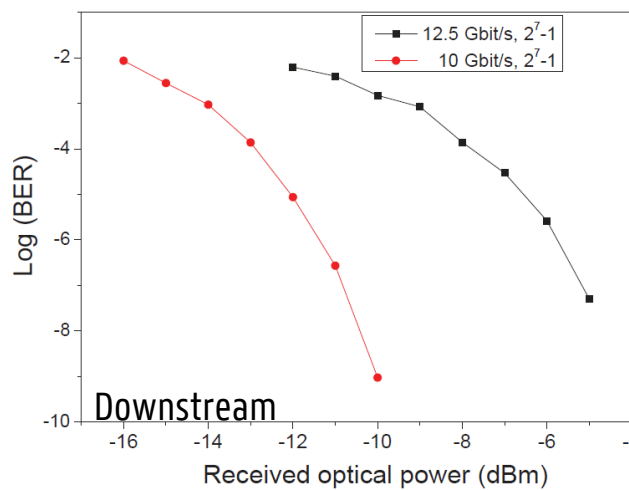
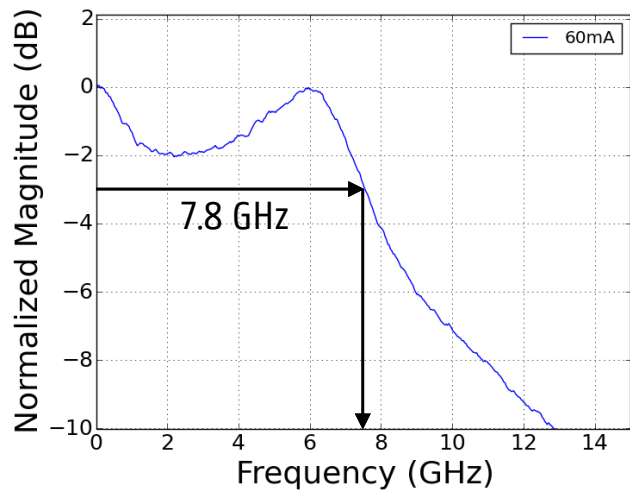
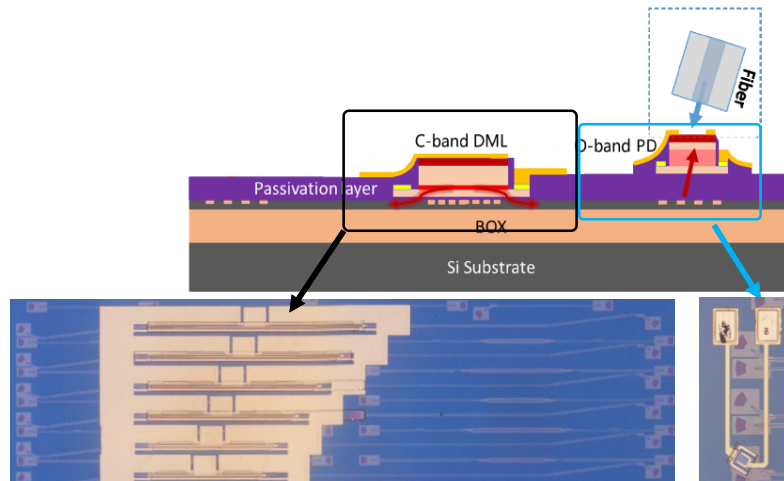
Signal format : NRZ-PRBS

DFB laser small signal 3-dB bandwidth: ~ 7.8 GHz

DFB fiber coupled power: 0.45 mW

Responsivity of the PD in 0-band: ~ 0.3 A/W

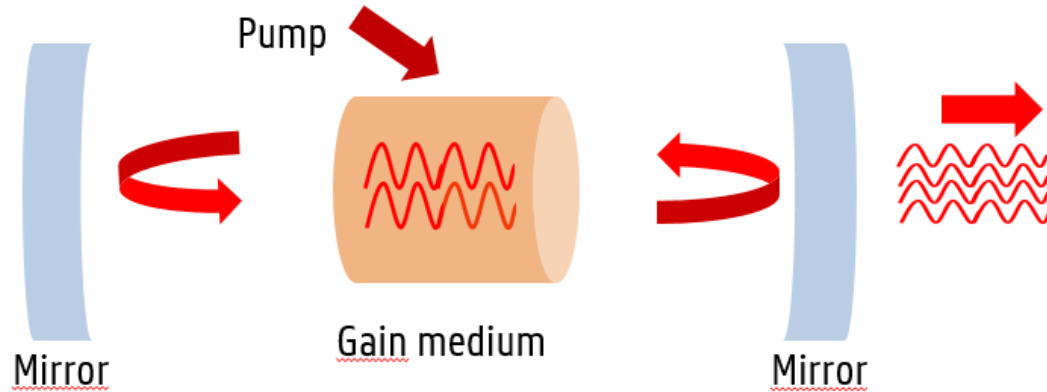
Error-free operation @ 10 Gbit/s



WHAT DID I DO IN MY PH.D YEARS?

1. Transfer-printed Fiber-To-The-Home (FTTH) transceivers
 1. Transfer printing O-band photodiodes
 2. Transfer-printed DFB lasers
 3. Four-channel point-to-point FTTH transceiver array
 4. A single-channel point-to-point FTTH transceiver based on the co-integration of DFB laser and O-band PD
2. Integration of III-V-on-Si coherent receivers through micro-transfer printing.
 1. **Transfer-printed widely tunable and narrow linewidth laser**
 2. Transfer-printed coherent receiver on a passive PIC
 3. **Integrated coherent receiver based on the imec iSiPP25/50G platform**

LASER SPECTRAL LINEWIDTH



$$\Delta\nu_{laser} = \frac{v_g^2 \hbar \nu g_{th} n_{sp} \alpha_m}{8\pi P} (1 + \alpha^2)$$

$$\alpha_m = -L^{-1} \ln(r_m)$$

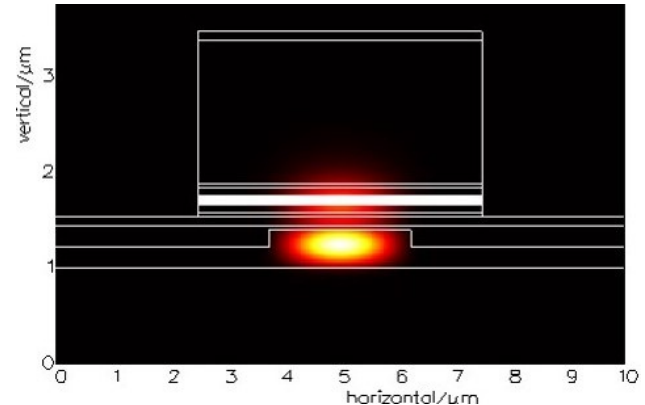
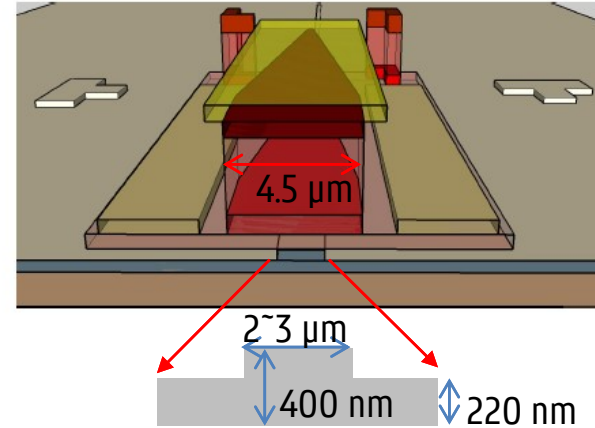
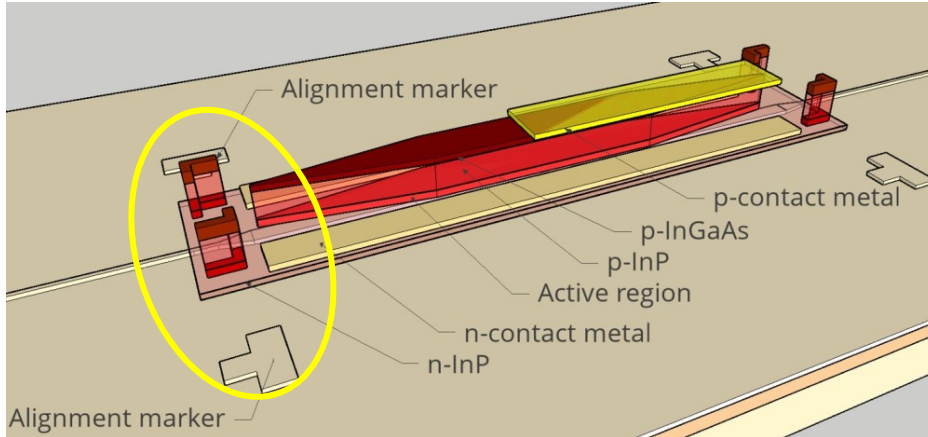


1. Low cavity loss (g_{th}, r_m)
2. Long cavity (L)
3. High output power (P)
4. Low linewidth broadening factor (α)
5. Stable pump

ALIGNMENT TOLERANT III-V/SI TAPER STRUCTURE

**Position tolerance of $\pm 1.5 \mu\text{m}$ at 3σ in large arrays
 $\pm 0.5 \mu\text{m}$ when printed in small arrays**

1. Reduce the optical confinement factor in III-V
2. Use a wide III-V waveguide



ALIGNMENT TOLERANT III-V/SI TAPER STRUCTURE

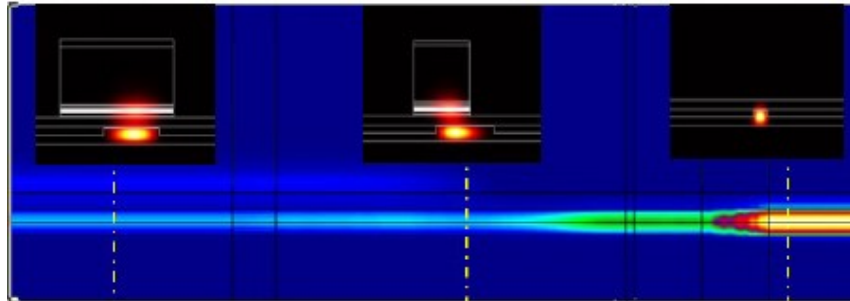
III-V waveguide width > 4.5 μm

Si waveguide width : 2 ~ 3 μm

BCB thickness < 60 nm

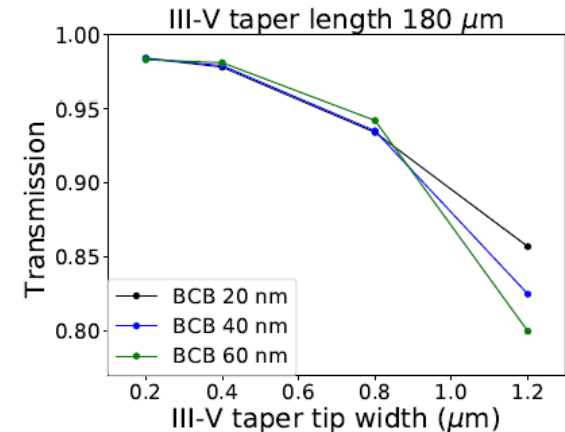
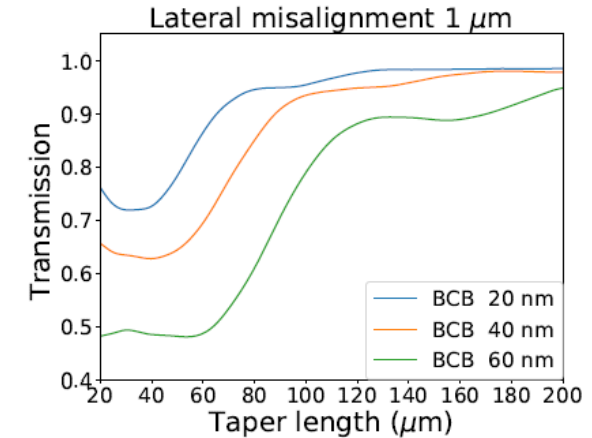
III-V taper tip width < 600 nm

III-V taper length > 180 μm



BCB: 20 nm

III-V taper length : 180 μm



PRE-FABRICATION OF SOAs ON THE INP SUBSTRATE

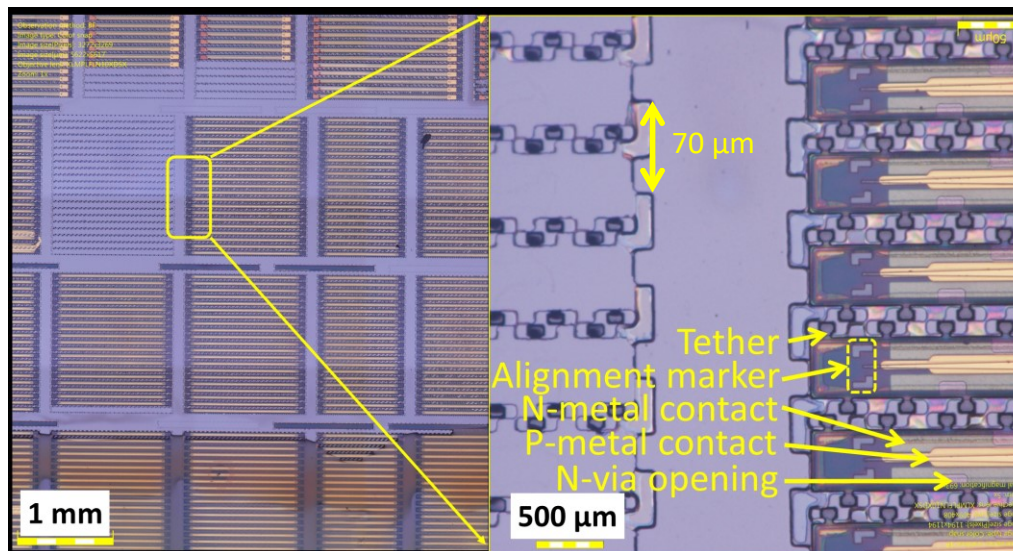
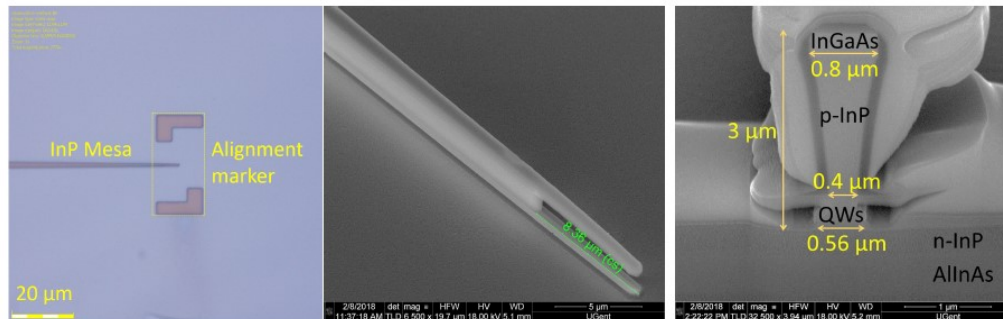
Release layer: 500 nm InAlAs

III-V mesa width: 4.5 μm

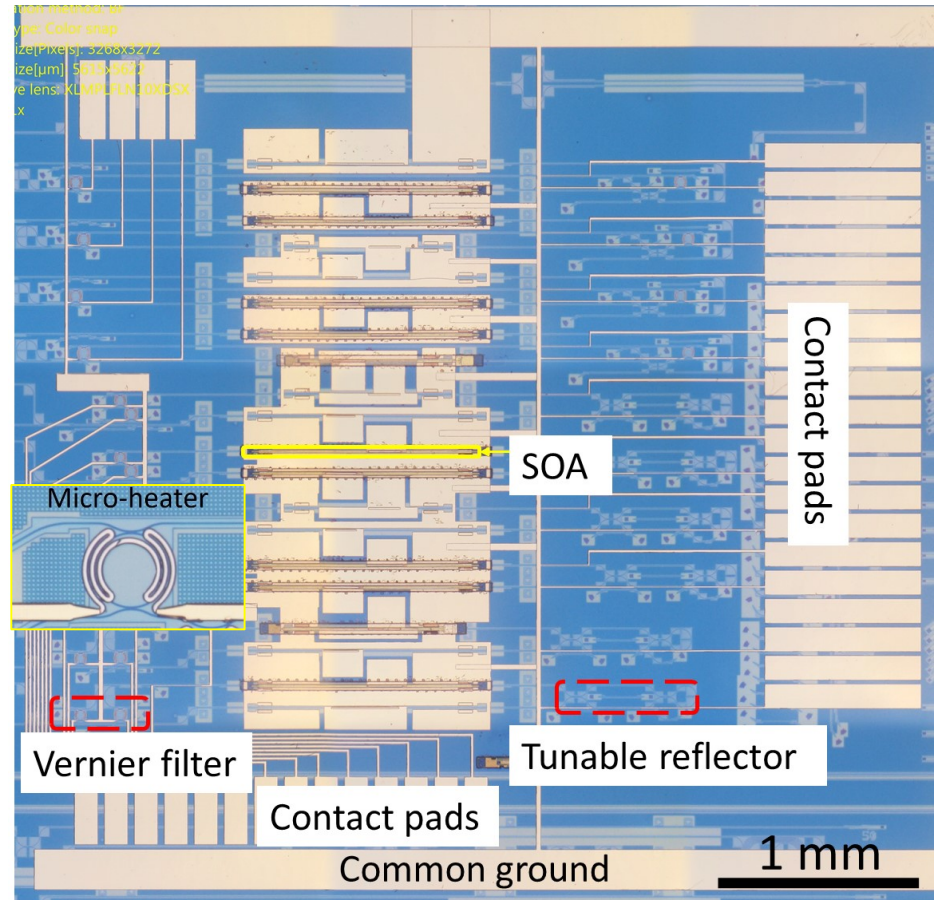
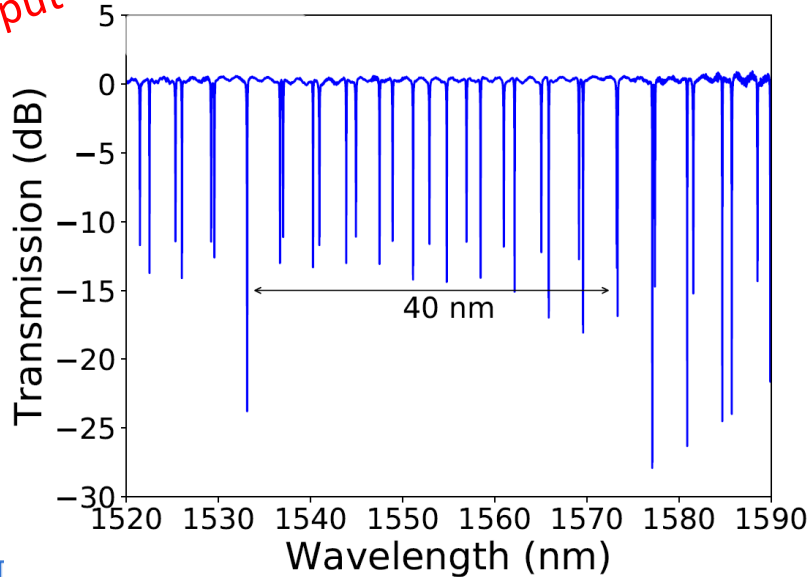
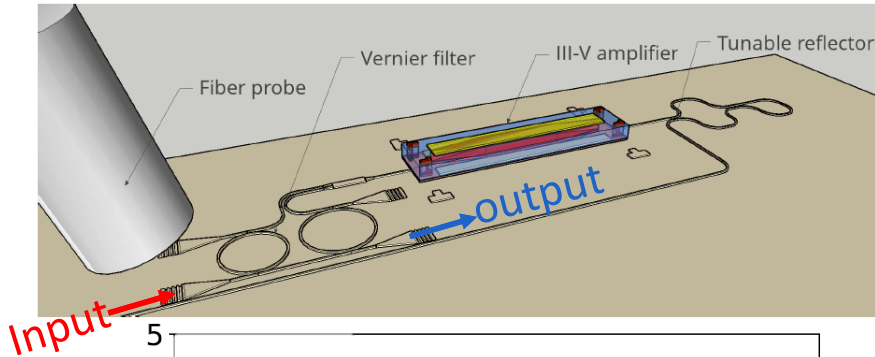
Taper length: 200 μm

Coupon pitch: 70 μm

SOA length : 1.16 mm/1.36mm



TRANSFER-PRINTED III-V-ON-SI WIDELY TUNABLE LASERS



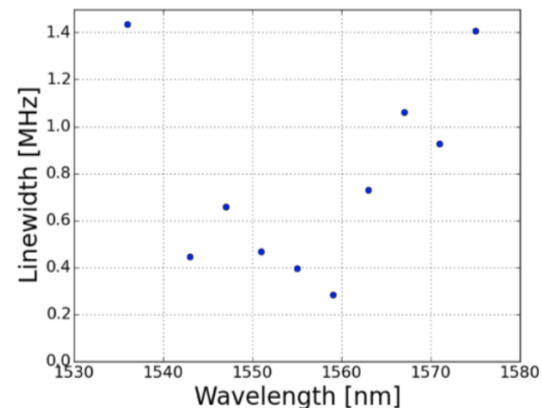
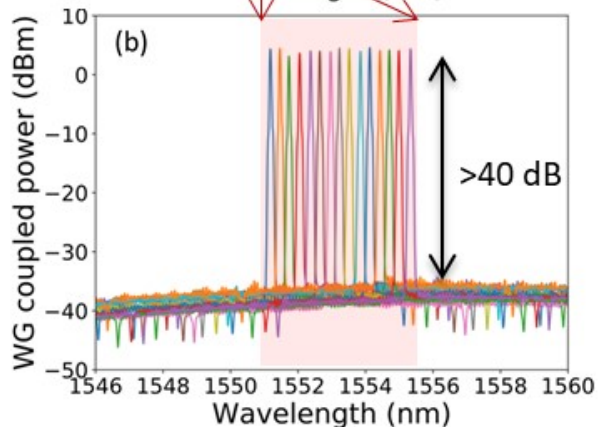
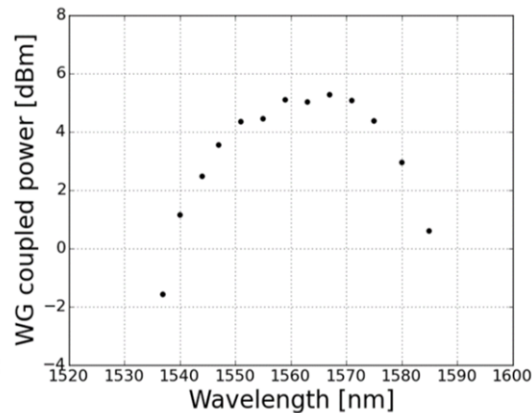
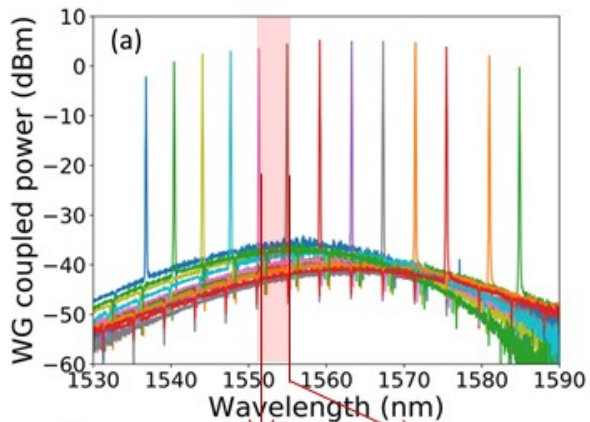
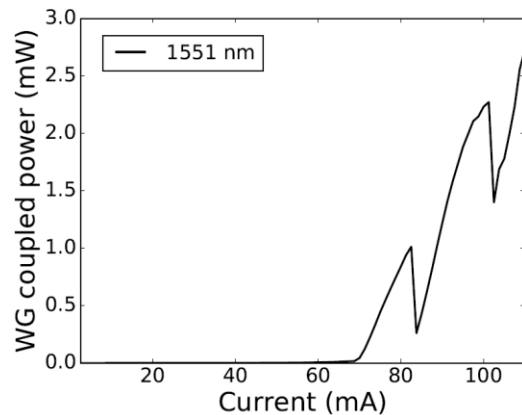
TRANSFER-PRINTED III-V-ON-SI WIDELY TUNABLE LASERS

Tuning range: 50 nm

SMSR: >40 dB

Peak output power: >5 dBm

Minimum Linewidth: 300 KHz

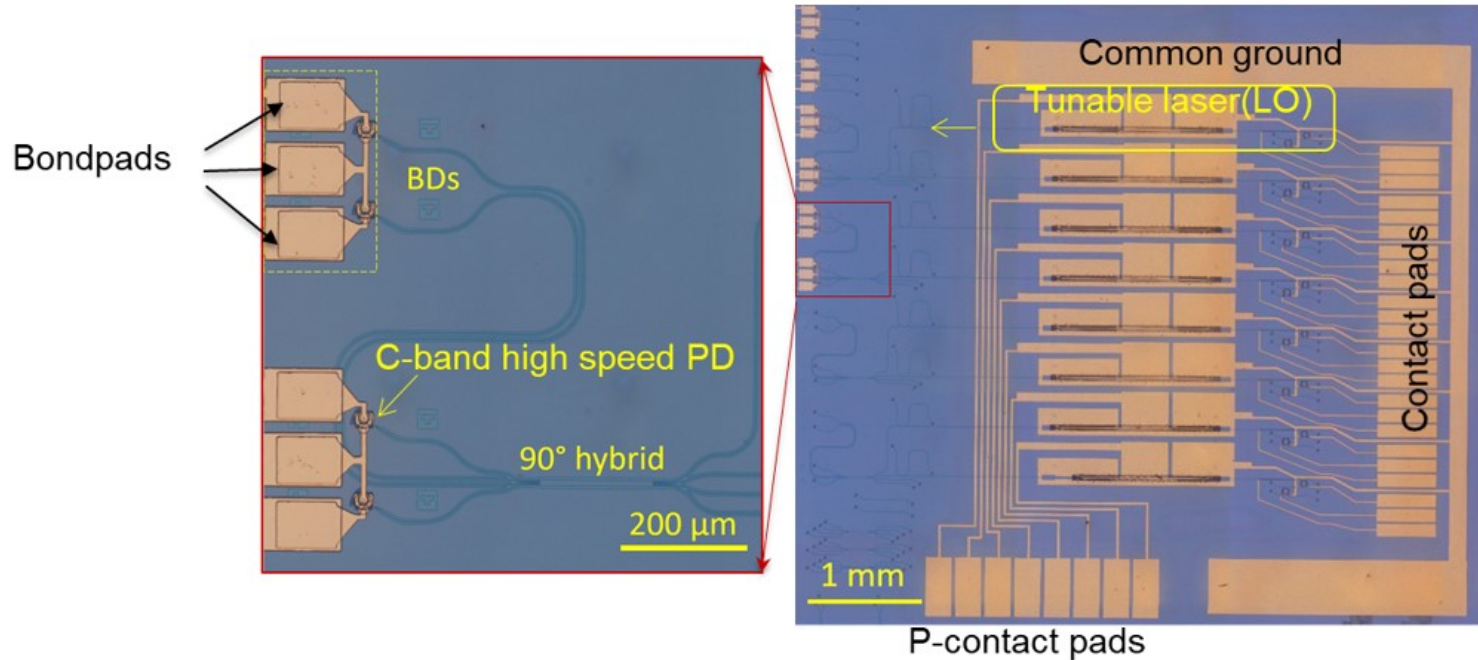


III-V-ON-SI INTEGRATED COHERENT RECEIVER (ICR) ON PASSIVE PICs

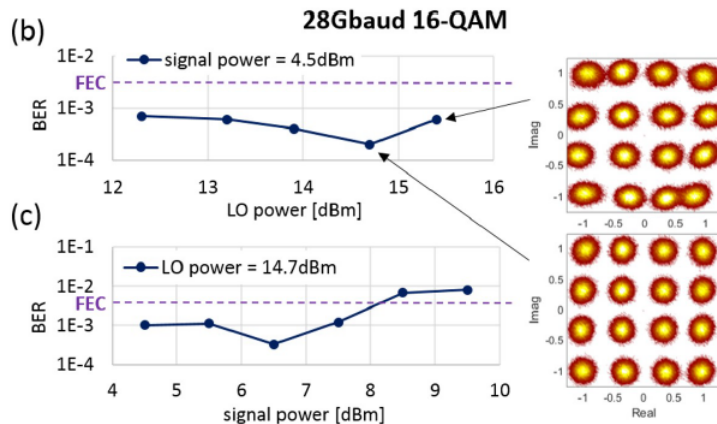
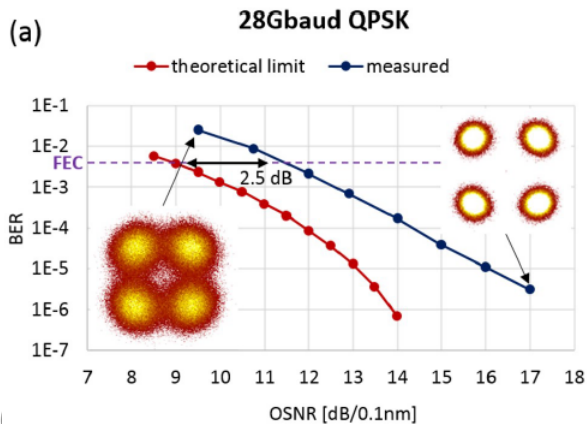
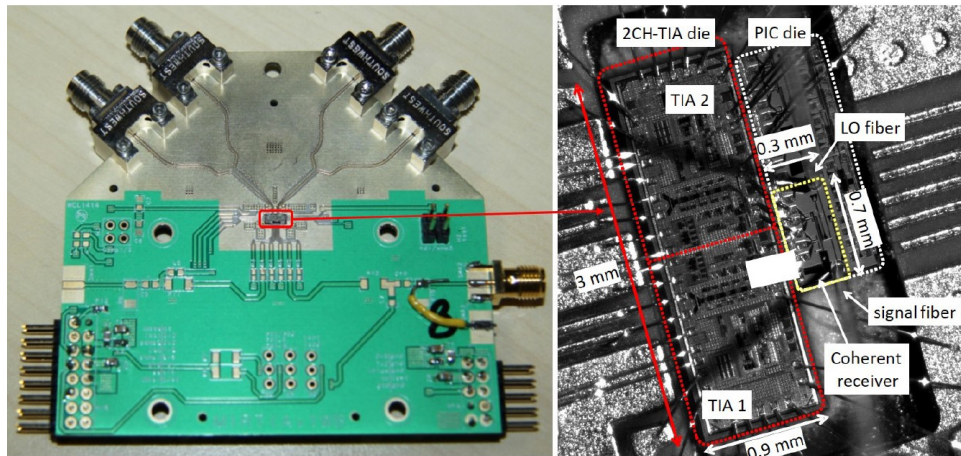
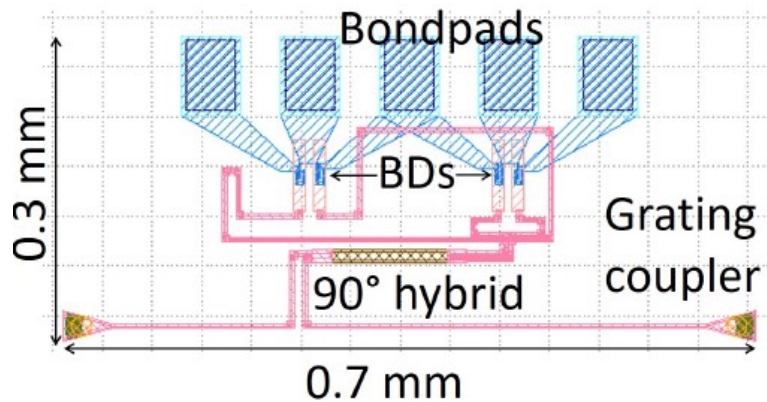
Si PICs:

Si device layer: 400 nm thick

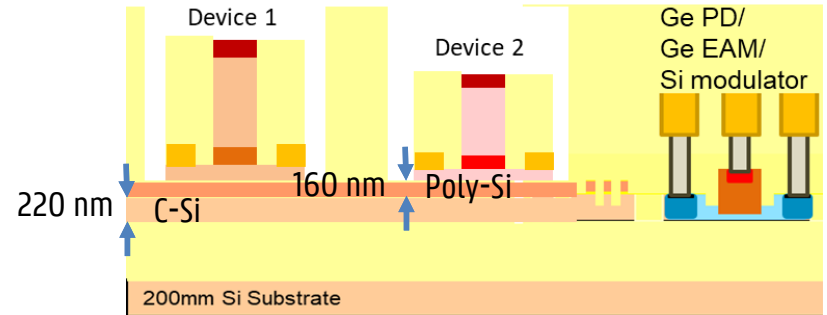
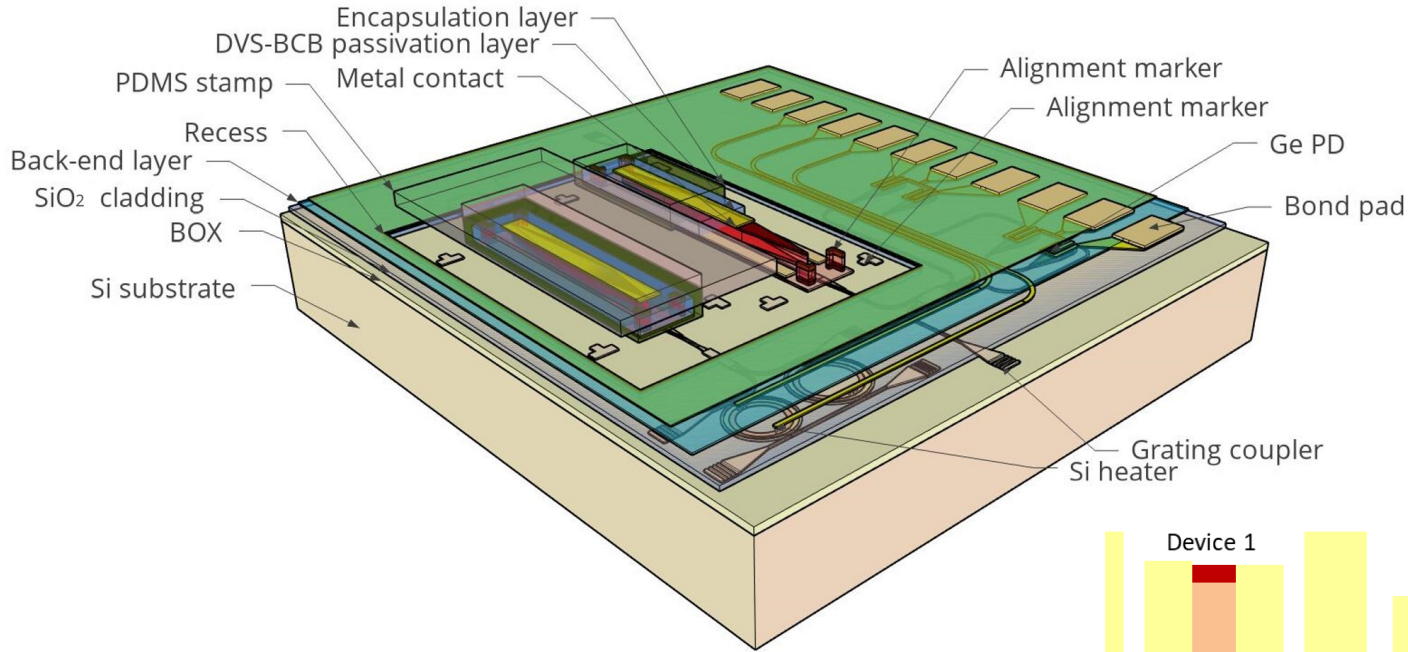
Etch step: 180 nm



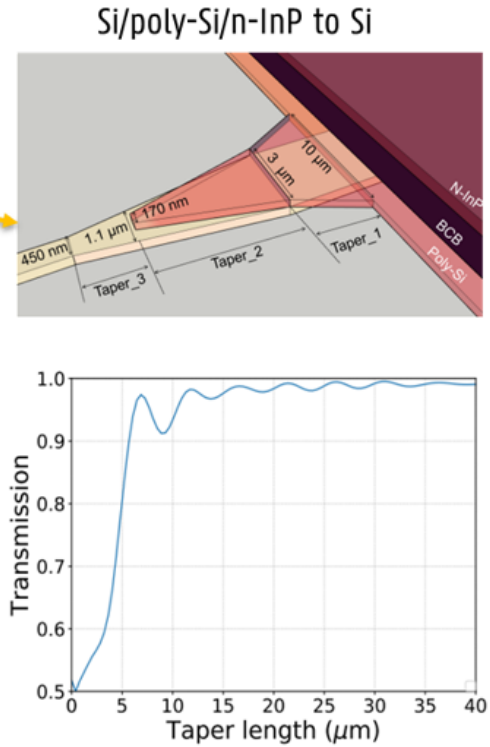
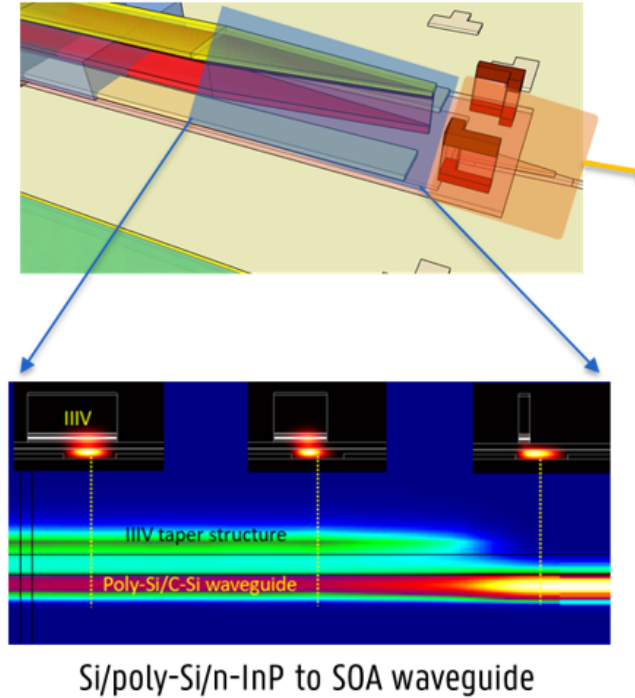
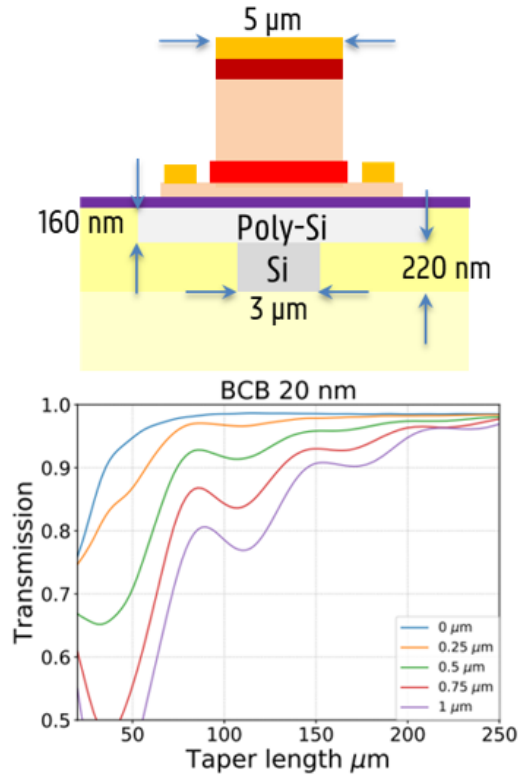
III-V-ON-Si ICR ON THE IMEC ISIPP25G/50G PLATFORM



III-V-ON-SI ICR ON THE IMEC ISIPP25G/50G PLATFORM



ALIGNMENT TOLERANT III-V/Si TAPER STRUCTURE



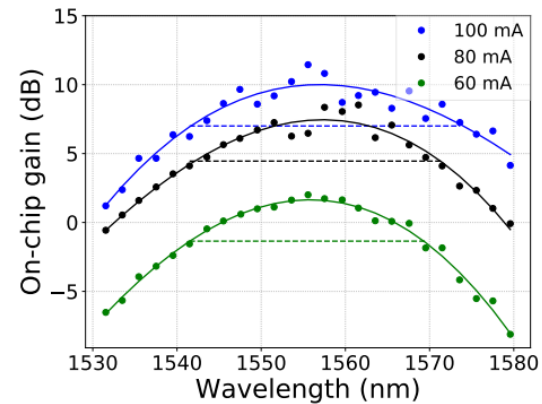
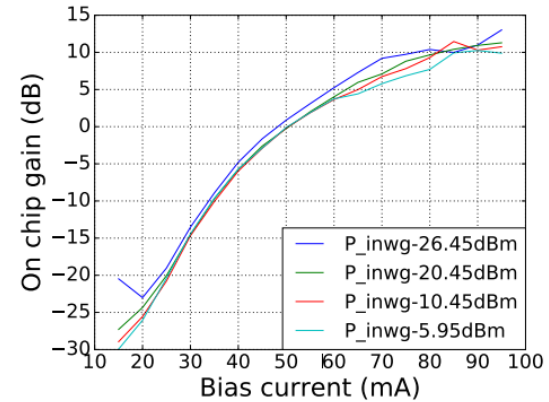
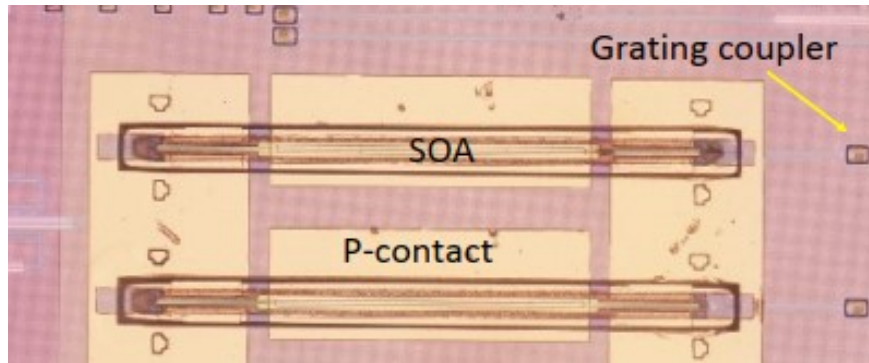
TRANSFER-PRINTED SOAs ON THE IMEC ISIPP50G PLATFORM

Spray coated BCB

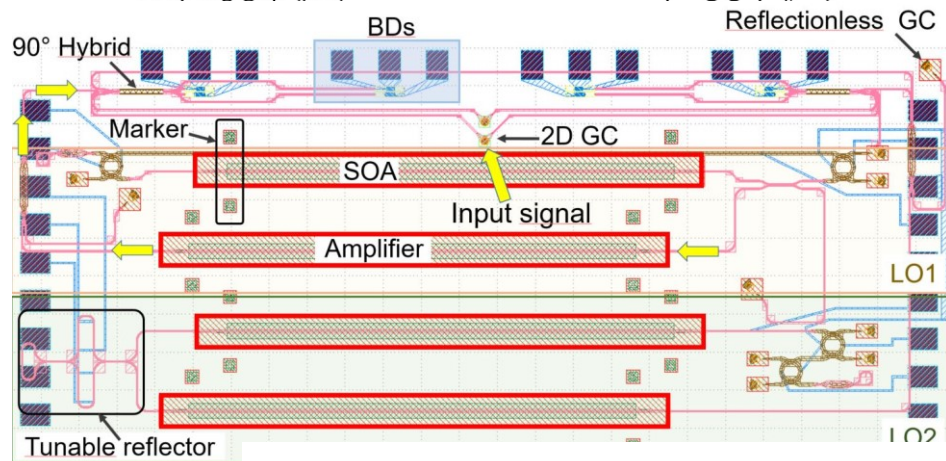
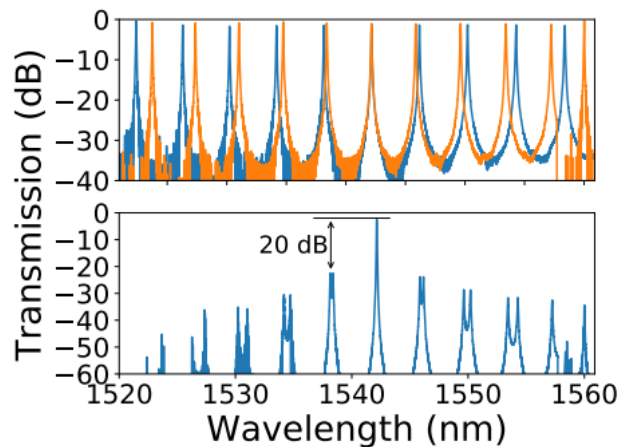
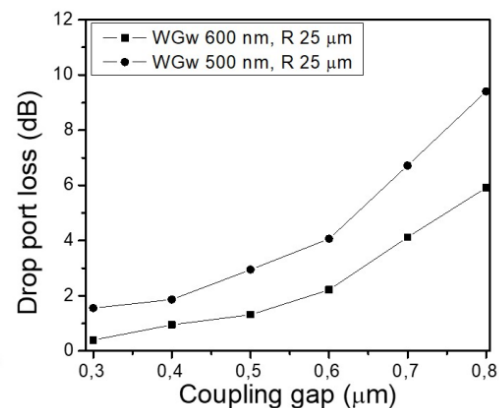
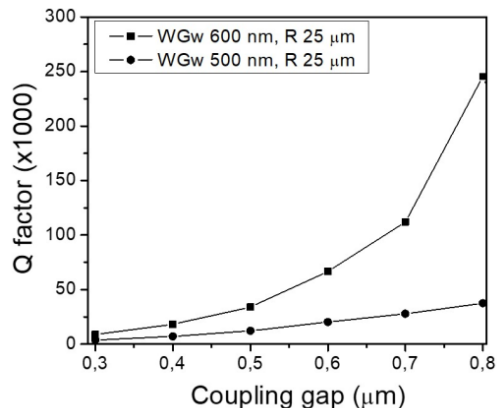
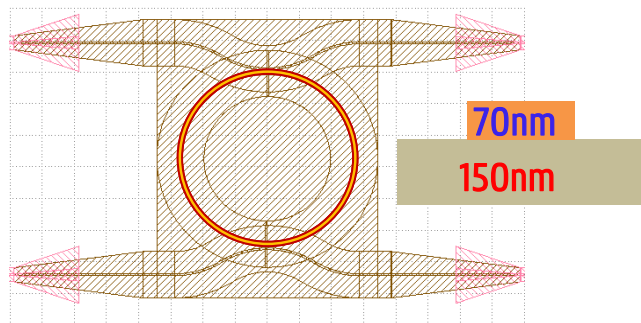
SOA length: ~1 mm

Small signal gain: 10dB @ 100mA

3dB gain bandwidth: 35 nm @100 mA



ICR DESIGNS ON THE IMEC ISIPP50G PLATFORM



We are ready for the next step!

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What is next:

- Complex C-band III-V/Si PICs based on iSiPP50G
- Move to O-band with InAs/GaAs QD structures
- Integration of other materials / on other photonic platforms



PHOTONICS RESEARCH GROUP

Jing Zhang

E jingzhan.Zhang@ugent.be

T +032465623752



@PhotonicsUGent

www.photonics.intec.ugent.be