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# Advanced Germanium p-i-n and Avalanche Photodetectors for Low-power Optical Interconnects

Hongtao Chen Supervisor: Prof. Gunther Roelkens, Dr. Joris Van Campenhout





### Motivation $\rightarrow$ Low-power optical interconnects





Optical interconnects in data center



#### Mega-scale cloud data centers



A commercial optical transceiver cartoon



#### High Sensitivity Optical Receiver to Improve Power efficiency



#### CMOS $4\lambda$ -CWDM OOK optical transceiver

<sup>\*</sup>M. Rakowski, et al, "A 4x20Gb/s WDM Ring-based Hybrid CMOS Silicon Photonics Transceiver", *ISSCC*, 408 (2015).



# Imec's silicon photonics platform



- State-of-the-art R&D platform for advanced device and system R&D
- 200 mm SOI wafers, 220 nm top Si, 160 nm polySi
- Integration Flow based on a 130-nm CMOS node/toolset augmented with 100% selective Ge epitaxy module
- 193-nm lithography for critical waveguide patterning steps.
- Available for bilateral development on demand and through MPW service (ePIXfab)



## Outline

- Motivation
- Si-contacted Ge p-i-n Photodetectors
  - o 400nm-Ge Si-LPIN GePD
  - 160nm-Ge Si-LPIN GePD
- Low-voltage Ge Avalanche Photodetectors
  - 400nm-Ge VPIN GeAPD
  - 185nm-Ge VPIN GeAPD
- Summary



#### Ge-on-Si Waveguide p-i-n photodetectors





#### Photo-carriers generation & collection





#### Responsivity

- The capability of a p-i-n photodetector to convert an optical signal into an photocurrent,
- The ratio of the generated photocurrent and incident optical power,
- Light absorption
- Photo-carriers collection



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#### Dark current

The small electric current that flows through a p-i-n photodetector when no photons are entering the device

- Diffusion current
- SRH leakage current
  - Material defects
  - Ge passivation



#### Ge-on-Si SEM graph



A typical photodiode I-V characteristic



SRH leakage current source modeling



### O/E bandwidth

The capability of a p-i-n photodetector to respond to a fast modulated optical signal.

- Transit time
- RC-constant



An ideal OOK modulated optical signal







p-i-n photodetector model



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#### 400 nm-Ge Si-LPIN GePD





#### Si-LPIN GePD:

- Higher responsivity
- Higher O/E bandwidth
- Lower dark current





#### Static, Small & Large-signal Measurement Data at 1550 nm

40

40

11 nA

>50 GHz

50

50



3 nA

20 GHz



28 Gb/s OOK-NRZ eye



**Dark current** 

O/E bandwidth

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### 160nm-Ge Si-LPIN GePD



3-D & cross sectional schematic



High responsivity & Low dark current



#### Static and Small-signal Measurement Data



50 GHz S<sub>21</sub> measurement

![](_page_15_Picture_3.jpeg)

# Large-signal Data Reception Measurement

Generating 80 Gb/s modulated optical signal through Optical Time Domain Multiplexing

![](_page_16_Figure_2.jpeg)

\*Implemented in DTU Fotonik, Denmark

![](_page_16_Picture_4.jpeg)

#### Large-signal Data Reception Measurement

70 GHz comerc. PD

at -1 V

(u2t XPDV-3120R)

at -2 V

#### 80 Gb/s eye diagrams

![](_page_17_Figure_5.jpeg)

![](_page_17_Figure_6.jpeg)

![](_page_17_Figure_7.jpeg)

#### 100 Gb/s eye diagrams

![](_page_17_Picture_9.jpeg)

Eye height: 34 mV

![](_page_17_Picture_11.jpeg)

Clear open eye diagrams obtained at 100 Gb/s

![](_page_17_Picture_13.jpeg)

#### Ge p-i-n PD: Benchmark

![](_page_18_Figure_2.jpeg)

![](_page_18_Picture_3.jpeg)

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![](_page_19_Picture_9.jpeg)

### Pursuing Even Higher Sensitivity By Leveraging Avalanche Multiplication

![](_page_20_Figure_1.jpeg)

\*Eduard Sackinger, Broadband Circuits for Optical Communications.

![](_page_20_Picture_3.jpeg)

#### Avalanche gain

![](_page_21_Figure_1.jpeg)

S-A-C-M Ge/Si Avalanche Photodiode

![](_page_21_Figure_3.jpeg)

 $\begin{bmatrix} 10^{5} \\ Ge (111) \\ Si (\alpha) \\ Si (\alpha) \\ Si (\beta) \\ Si (\beta) \\ Ge (111) \\ Si (\alpha) \\ Si (\beta) \\ Si (\beta) \\ Ge (111) \\ G$ 

#### Impact ionization

#### Impact ionization coefficient:

(α, electrons; β, holes)the number of electron-hole

pairs generated by a carrier per unit distance traveled

![](_page_21_Picture_9.jpeg)

#### Gain-bandwidth product ← build-up time

![](_page_22_Figure_1.jpeg)

O/E bandwidth as a function of gain

\*Simon M. Sze, Physics of Semiconductor Devices

![](_page_22_Picture_4.jpeg)

#### Excess noise factor

![](_page_23_Figure_1.jpeg)

![](_page_23_Figure_2.jpeg)

Avalanche sensitivity improvement

$$(i_{S})^{2} = 2 \times q \times I_{0} \times M^{2} \times F(M) \times B$$
  
Noise current  
power  
Gain  
Excess noise  
factor

![](_page_23_Picture_5.jpeg)

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![](_page_24_Picture_9.jpeg)

#### 400 nm-Ge VPIN GeAPD

![](_page_25_Figure_1.jpeg)

![](_page_25_Picture_2.jpeg)

3-D & cross sectional schematic

#### Doping concentration (cm<sup>-3</sup>)

![](_page_25_Figure_5.jpeg)

#### ElectricField (V/cm)

![](_page_25_Figure_7.jpeg)

Doping & electric field distribution

- |E| ~ 2×10<sup>5</sup> V/cm confined in the bottom 200 nm Ge layer at -5.5 V bias,
- $\rightarrow$  Strong avalanche multiplication expected,

![](_page_25_Picture_11.jpeg)

![](_page_26_Figure_0.jpeg)

Extracted from raw S<sub>21</sub> curves

![](_page_26_Picture_2.jpeg)

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#### **Optical Receiver Sensitivity Measurement Data**

![](_page_27_Picture_1.jpeg)

- \*Custom TIA design from INTEC\_design,
- ✓ 130 nm SiGe BiCMOS technology,
- ✓ 1.2 µA input referred (RMS) noise current at 10 Gb/s,
- (2<sup>31</sup>-1) PRBS NRZ modulation
- Operate at 1550 nm,
- Commercial LA used after TIA,

<sup>\*</sup>X. Yin et al., IEEE ISSCC Dig. Tech. Papers, 416 (2012).

![](_page_27_Figure_9.jpeg)

![](_page_27_Figure_10.jpeg)

- 10 Gb/s bit error ratios for various bias voltages
- 5.8 dB avalanche sensitivity improvement at -5.9 V APD bias
- -23.2 dBm absolute sensitivity

![](_page_27_Picture_14.jpeg)

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Summary

![](_page_28_Picture_9.jpeg)

### 185 nm-Ge VPIN GeAPD

![](_page_29_Figure_1.jpeg)

3-D and cross sectional schematic

 $\rightarrow$  Better avalanche performance expected

![](_page_29_Figure_4.jpeg)

![](_page_29_Figure_5.jpeg)

![](_page_29_Figure_6.jpeg)

Doping & electric field distribution

![](_page_29_Picture_8.jpeg)

#### Avalanche performance

![](_page_30_Figure_1.jpeg)

![](_page_30_Picture_2.jpeg)

![](_page_30_Figure_3.jpeg)

![](_page_30_Picture_4.jpeg)

![](_page_30_Picture_5.jpeg)

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#### Avalanche performance

![](_page_31_Figure_1.jpeg)

![](_page_31_Picture_2.jpeg)

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### **Optical Receiver Sensitivity Measurement Data**

![](_page_32_Figure_1.jpeg)

\*B. Moeneclaey, et al., IEEE PTL, 27(13), 1375 (2015)

![](_page_32_Picture_3.jpeg)

#### Ge APD: Benchmark

![](_page_33_Figure_1.jpeg)

![](_page_33_Picture_2.jpeg)

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![](_page_34_Picture_9.jpeg)

#### Summary

### High performance Ge p-i-n PD demonstrated,

-1 V	Responsivity (A/W)		O/E bandwidth (GHz)		Dark current
	1550 nm	1310 nm	1550 nm	1310 nm	
400-nm Ge	> 1 A/W in the C-band		20	NA	3 nA
160-nm Ge	0.74	0.92	67	44	3 nA

#### Low-voltage Ge APD demonstrated,

	Gain-bandwidth product (GHz)	Avalanche sensitivity improvement (dB)	Absolute sensitivity (dBm)
*400-nm Ge	100	5.8	-23.2
**185-nm Ge	140	6.2	-17.4

*- 10 Gb/s at -5.9 V APD bias (1550 nm)	**- 20 Gb/s at -5 V APD bias (1310 nm)
- TIA input referred (RMS) noise	- TIA input referred (RMS) noise
current, 1.2 μA;	current, 2.0 μA;

![](_page_35_Picture_6.jpeg)

### Acknowledgement

- IMEC Si photonics Team,
  - Joris Van Campenhout, Peter Verheyen, Geert Hellings, Jeroen De Coster, Guy Lepage, ...
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![](_page_36_Picture_6.jpeg)